# **IGBT - Field Stop, Trench**

75 A, 950 V

# **Product Preview**

# FGY75T95LQDT

Trench Field Stop 4<sup>th</sup> generation Low Vcesat IGBT co-packaged with full current rated diode.

#### **Features**

- Maximum Junction Temperature : T<sub>J</sub> = 175°C
- Positive Temperature Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage:  $V_{CE(Sat)} = 1.31 \text{ V (Typ.)} @ I_C = 75 \text{ A}$
- Fast Switching
- Tighten Parameter Distribution
- These Devices are Pb-Free and are RoHS Compliant

## **Applications**

• Solar Inverter

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit	
Collector to Emitter Voltage		V <sub>CES</sub>	950	V
Gate to Emitter Voltage Transient Gate to Emitter Voltage		$V_{GES}$	±20 ±30	٧
Collector Current $@T_C = @T_C =$	Ic	150 75	A	
Pulsed Collector Current (Note 1)	I <sub>LM</sub>	225	Α	
Pulsed Collector Current (Note 2)		I <sub>CM</sub>	225	Α
Diode Forward Current $@T_C = 25^{\circ}C$ $@T_C = 100^{\circ}C$		lF	150 75	Α
Pulsed Diode Forward Current (Note 2)		I <sub>FM</sub>	225	Α
Maximum Power Dissipation $@T_C = 25^{\circ}C$ $@T_C = 100^{\circ}C$		PD	453 226	W
Operating Junction / Storage Temperature Range		TJ, TSTG	-55 to +175	°C
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds		T <sub>L</sub>	300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. VCC = 700 V, VGE = 15 V, IC = 225 A, RG = 26  $\Omega$ , Inductive Load, 100% Tested
- Pulse width limited by max Junction temperature. Defined by design. Not subject to production test

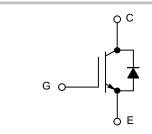
This document contains information on a product under development. ON Semiconductor reserves the right to change or discontinue this product without notice.

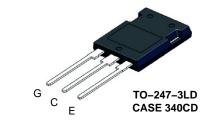


## ON Semiconductor®

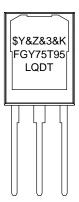
www.onsemi.com

75 A, 950 V V<sub>CESat</sub> = 1.31 V (Typ.)





## **MARKING DIAGRAM**



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Numeric Date Code &K = 2-Digit Lot Traceability Code

FGY75T95LQDT = Specific Device Code

## ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

## **ORDERING INFORMATION**

Part Number	Top Marking	Package	Shipping
FGY75T95LQDT	FGY75T95LQDT	TO-247-3LD (Pb-Free)	30 Units / Rail

## THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ heta JC}$	0.33	°C/W
Thermal resistance junction-to-case, for Diode	$R_{ heta JC}$	0.23	°C/W
Thermal resistance junction–to–ambient	$R_{ heta JA}$	40	°C/W

## **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Collector–emitter breakdown voltage, gate–emitter short–circuited	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 1 mA	BVCES	950			V
Temperature Coefficient of Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 1 mA	ΔBVCES ΔΤ <sub>J</sub>		0.96		V/°C
Collector–emitter cut–off current, gate– emitter short–circuited	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 950 V	ICES			250	μΑ
Gate leakage current, collector—emitter short–circuited	V <sub>GE</sub> = 20 V , V <sub>CE</sub> = 0 V	IGES			±400	nA
ON CHARACTERISTICS		1	•	•	•	
Gate-emitter threshold voltage	$V_{GE} = V_{CE}$ , $I_C = 75$ mA	VGE(th)	3.4	4.57	6.4	V
Collector–emitter saturation voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 75 A V <sub>GE</sub> = 15 V, I <sub>C</sub> = 75 A, T <sub>J</sub> = 175°C	VCE(sat)		1.31 1.52	1.69	V
DYNAMIC CHARACTERISTICS		1	•	•	•	
Input capacitance	V <sub>CE</sub> = 30 V, V <sub>GE</sub> = 0 V, f = 1 MHz	Cies		15400		pF
Output capacitance		Coes		266		
Reverse transfer capacitance		Cres		85.3		
Gate charge total	$V_{CE} = 600 \text{ V}, I_{C} = 75 \text{ V}, V_{GE} = 15 \text{ V}$	Qg		663.3		nC
Gate to emitter charge		Qge		76.1		
Gate to collector charge		Qgc		218.6		
SWITCHING CHARACTERISTICS, INDUC	CTIVE LOAD					
Turn-on delay time	T <sub>J</sub> = 25°C	td(on)		52.0		ns
Rise time	$V_{CC} = 600 \text{ V, I}_{C} = 37.5 \text{ A}$ $Rg = 4.7 \Omega$	t <sub>r</sub>		24.0		
Turn-off delay time	V <sub>GE</sub> = 15 V Inductive Load	td(off)		496.0		
Fall time	inductive Load	t <sub>f</sub>		108.0		
Turn-on switching loss		Eon		2.0		mJ
Turn-off switching loss		Eoff		1.8		
Total switching loss		Ets		3.7		

## **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
Turn-on delay time	T <sub>J</sub> = 25°C	td(on)		52.0		ns
Rise time	$V_{CC} = 600 \text{ V, } I_{C} = 75 \text{ A}$ $Rg = 4.7 \Omega$	t <sub>r</sub>		52.0		
Turn-off delay time	V <sub>GE</sub> = 15 V Inductive Load	td(off)		476.0		
Fall time	maddivo Edad	t <sub>f</sub>		76.0		
Turn-on switching loss		Eon		4.8		mJ
Turn-off switching loss		Eoff		3.3		
Total switching loss		Ets		8.1		
Turn-on delay time	T <sub>J</sub> = 175°C	td(on)		44.0		ns
Rise time	$V_{CC} = 600 \text{ V}, I_{C} = 37.5 \text{ A}$ $Rg = 4.7 \Omega$	t <sub>r</sub>		30.0		
Turn-off delay time	V <sub>GE</sub> = 15 V	td(off)		580.0		
Fall time	inductive Load	t <sub>f</sub>		144.0		
Turn-on switching loss		Eon		3.8		mJ
Turn-off switching loss		Eoff		2.7		
Total switching loss		Ets		6.5		
Turn-on delay time	T <sub>J</sub> = 175°C	td(on)		48.0		ns
Rise time	$V_{CC} = 600 \text{ V, } I_{C} = 75 \text{ A}$ $Rg = 4.7 \Omega$	t <sub>r</sub>		54.0		
Turn-off delay time	V <sub>GE</sub> = 15 V Inductive Load	td(off)		548.0		
Fall time	middelive Load	t <sub>f</sub>		118.0		
Turn-on switching loss		Eon		7.6		mJ
Turn-off switching loss		Eoff		5.1		
Total switching loss		Ets		12.7		
DIODE CHARACTERISTICS						
Forward voltage	I <sub>F</sub> = 75 A I <sub>F</sub> = 75 A, T <sub>J</sub> = 175°C	V <sub>F</sub>		2.03 1.76	2.51	V
Reverse Recovery Energy	T <sub>J</sub> = 25°C	E <sub>rec</sub>		314		uJ
Reverse Recovery Time	$V_R = 600 \text{ V}, I_F = 37.5 \text{ A}$ $dI_F/dt = 1000 \text{ A}/\mu\text{s}$	t <sub>rr</sub>		105		ns
Reverse Recovery Charge		Q <sub>rr</sub>		1635		nC
Reverse Recovery Energy	T <sub>J</sub> = 25°C	E <sub>rec</sub>		2390		uJ
Reverse Recovery Time	$V_R = 600 \text{ V}, I_F = 75 \text{ A}$ $dI_F/dt = 1000 \text{ A}/\mu\text{s}$	t <sub>rr</sub>		259		ns
Reverse Recovery Charge	.,	Q <sub>rr</sub>		7515		nC
Reverse Recovery Energy	T <sub>J</sub> = 175°C	E <sub>rec</sub>		454		uJ
Reverse Recovery Time	$V_R = 600 \text{ V}, I_F = 37.5 \text{ A}$ $dI_F/dt = 1000 \text{ A}/\mu\text{s}$	t <sub>rr</sub>		148		ns
Reverse Recovery Charge		Q <sub>rr</sub>		2436		nC

## **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
Reverse Recovery Energy	T <sub>J</sub> = 175°C	E <sub>rec</sub>		2790		uJ
Reverse Recovery Time	$V_R = 600 \text{ V}, I_F = 75 \text{ A}$ $dI_F/dt = 1000 \text{ A}/\mu\text{s}$	t <sub>rr</sub>		294		ns
Reverse Recovery Charge		Q <sub>rr</sub>		9175		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

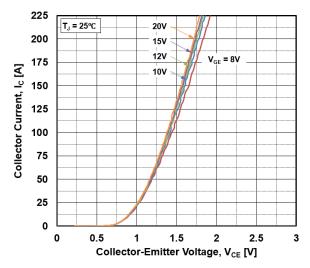


Figure 1. Typical Output Characteristics ( $T_J = 25^{\circ}C$ )

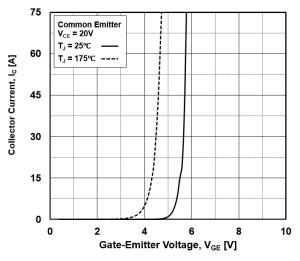


Figure 3. Transfer Characteristics

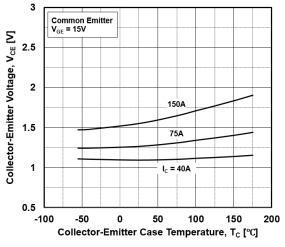


Figure 5. Saturation Voltage vs Case Temperature at Variant Current Level

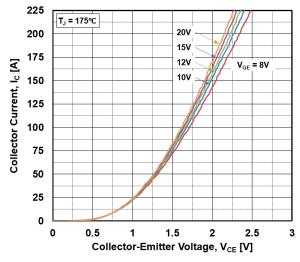


Figure 2. Typical Output Characteristics ( $T_J = 175^{\circ}C$ )

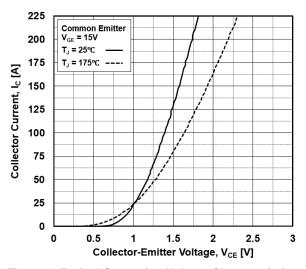


Figure 4. Typical Saturation Voltage Characteristics

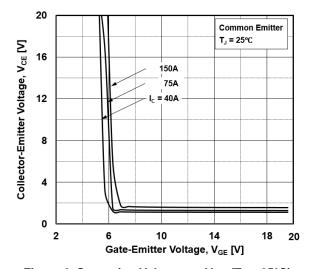


Figure 6. Saturation Voltage vs.  $V_{GE}$  (T<sub>J</sub> = 25°C)

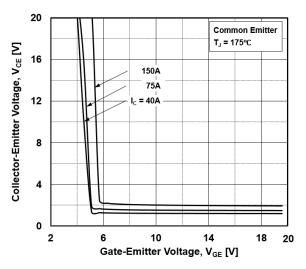


Figure 7. Saturation Voltage vs.  $V_{GE}$  (T<sub>J</sub> = 175°C)

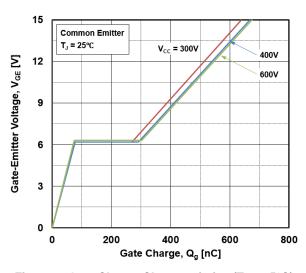


Figure 9. Gate Charge Characteristics ( $T_J = 25^{\circ}C$ )

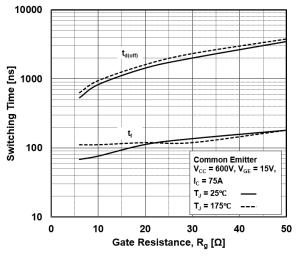


Figure 11. Turn-off Characteristics vs. Gate Resistance

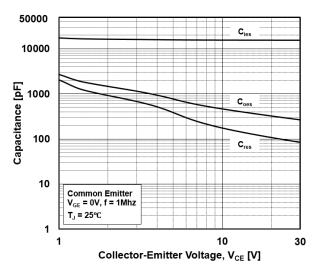


Figure 8. Capacitance Characteristics

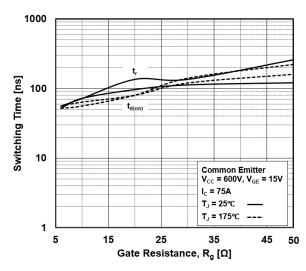


Figure 10. Turn-on Characteristics vs. Gate Resistance

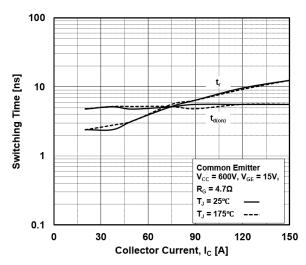


Figure 12. Turn-on Characteristics vs. Collector Current

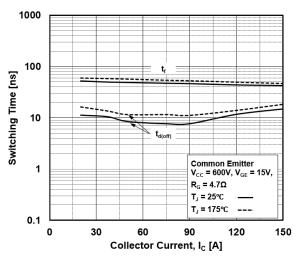


Figure 13. Turn-off Characteristics vs. Collector Current

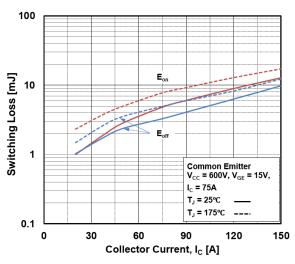


Figure 15. Switching Loss vs. Collector Current

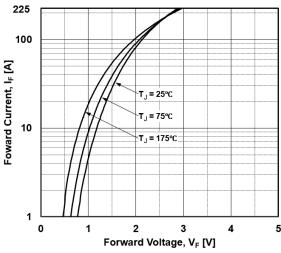


Figure 17. (Diode) Forward Characteristics vs (Normal I–V)

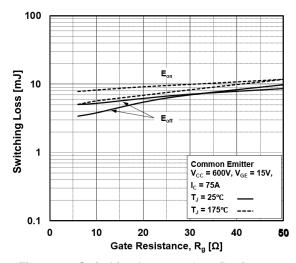


Figure 14. Switching Loss vs. Gate Resistance

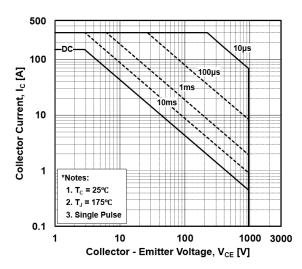


Figure 16. SOA Characteristics (FBSOA)

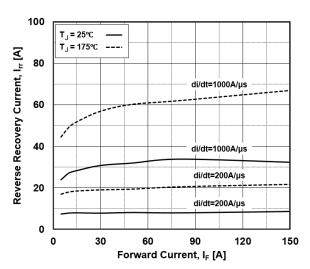
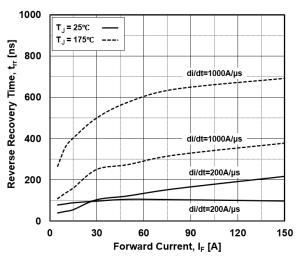


Figure 18. (Diode) Reverse Recovery Current



15000 T<sub>J</sub> = 25°C T<sub>J</sub> = 175℃ Reverse Recovery Charge, Q<sub>rr</sub> [nC] 12000 di/dt=1000A/µs 9000 di/dt=1000A/µs 6000 di/dt=200A/µs 3000 di/dt=200A/µs 0 0 30 60 90 120 150 Forward Current, I<sub>F</sub> [A]

Figure 19. (Diode) Reverse Recovery Time

Figure 20. (Diode) Stored Charge

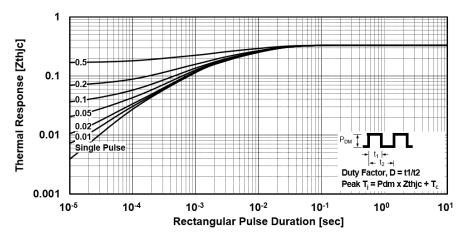


Figure 21. Transient Thermal Impedance of IGBT

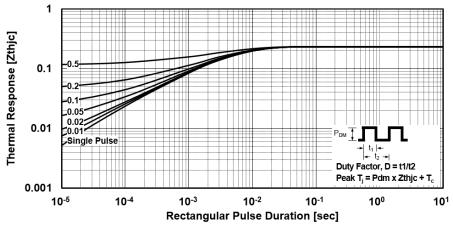


Figure 22. Transient Thermal Impedance of Diode



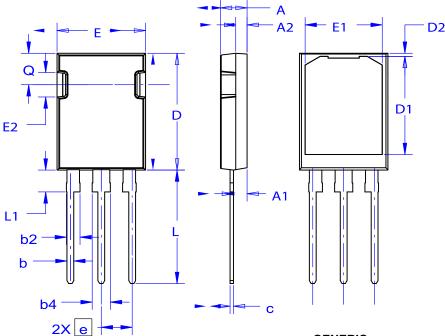


TO-247-3LD CASE 340CD ISSUE A

**DATE 18 SEP 2018** 

### NOTES:

- A. THIS PACKAGE DOES NOT CONFORM TO ANY STANDARDS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- D. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.



DIM	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	4.58	4.70	4.82	
<b>A</b> 1	2.20	2.40	2.60	
A2	1.80	2.00	2.20	
D	20.32	20.57	20.82	
Е	15.37	15.62	15.87	
E2	4.12	4.32	4.52	
е	~	5.45	~	
L	19.90	20.00	20.10	
L1	3.69	3.81	3.93	
Q	5.34	5.46	5.58	
b	1.10	1.20	1.30	
b2	2.10	2.24	2.39	
b4	2.87	3.04	3.20	
С	0.51	0.61	0.71	
D1	16.63	16.83	17.03	
D2	0.51	0.93	1.35	
E1	13.40	13.60	13.80	

AALLIAAETEDO

# GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

DOCUMENT NUMBER:	98AON13857G	Electronic versions are uncontrolled except when accessed directly from the Document Reported versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	TO-247-3LD		PAGE 1 OF 1	

onsemi and ONSeMi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, ONSEMI., and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems. or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$ 

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales