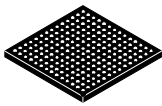


# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

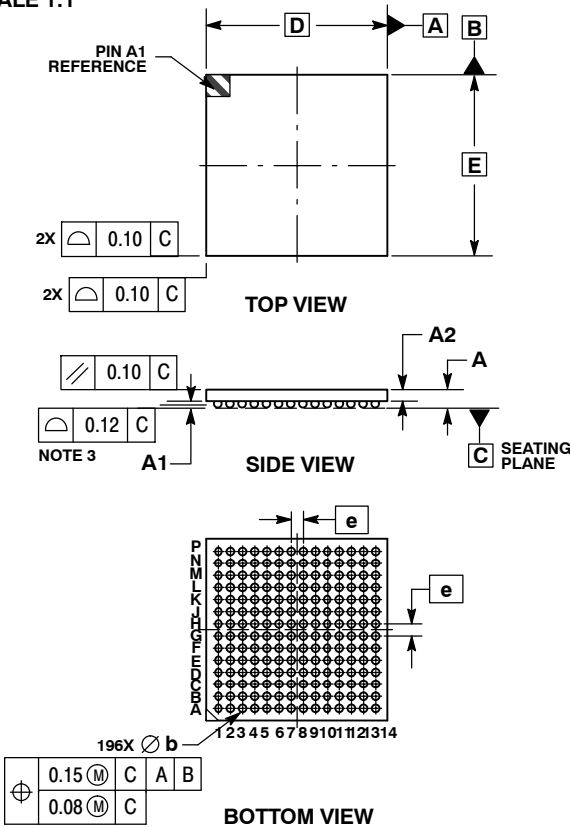
ON Semiconductor®



**BGA196, 12x12**  
**CASE 504AB-01**  
**ISSUE A**

DATE 18 NOV 2010

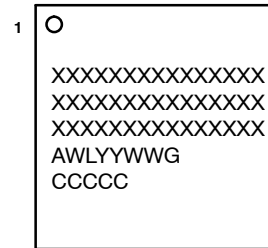
SCALE 1:1



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.

DIM	MILLIMETERS	
	MIN	MAX
A	1.20	1.40
A1	0.25	0.35
A2	1.00 REF	
b	0.37	0.42
D	12.00 BSC	
E	12.00 BSC	
e	0.80 BSC	

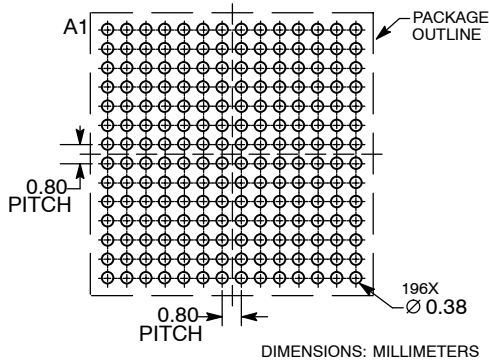
### GENERIC MARKING DIAGRAM\*



- XXXXXX = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present.

### RECOMMENDED SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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<b>NEW STANDARD:</b>		
<b>DESCRIPTION:</b>	<b>BGA196, 12X12</b>	<b>PAGE 1 OF 2</b>



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