



PRODUCT BULLETIN # 20762Generic Copy

Issue Date: 04-Mar-2015**TITLE:** LV8127 Datasheet Update**PROPOSED FIRST SHIP DATE:** 11-Jun-2015**AFFECTED CHANGE CATEGORY(S):** Datasheet Update**FOR ANY QUESTIONS CONCERNING THIS NOTIFICATION:**

Contact your local ON Semiconductor sales office

NOTIFICATION TYPE:

ON Semiconductor considers this change approved unless specific conditions of acceptance are provided in writing. To do so, contact <quality@onsemi.com>.

DESCRIPTION AND PURPOSE:

1. Detailed technical description is added.
 - a. The low voltage shutdown circuit operation when power supply voltage is drops.
 - b. Example of a waveform and plots to explain power FET gate driver current in an actual application.

The purpose is to show the above detailed information to the customer. Therefore, no device specification itself is changed.

2. Typo is fixed.
 - a. To fix the wrong description of the configuration pin logic state L/H for 'HSEL' the hall sensor location selection, which is described in the equivalent IO circuit table.

List of affected General Parts:LV8127T-MPB-H
LV8127T-TLM-H