

PRODUCT BULLETIN # 16645

Generic Copy

Issue Date: 11-May-2011

TITLE: NCP1250 / NCP1251 lcc Latch current adjustment

PROPOSED FIRST SHIP DATE: 11-Aug-2011

AFFECTED CHANGE CATEGORY(S): Data Sheet specification change

FOR ANY QUESTIONS CONCERNING THIS NOTIFICATION:

Contact your local ON Semiconductor sales office or Alois Chumchal <alois.chumchal@onsemi.com>

NOTIFICATION TYPE:

ON Semiconductor considers this change approved unless specific conditions of acceptance are provided in writing. To do so, contact <quality@onsemi.com>.

DESCRIPTION AND PURPOSE:

ON Semiconductor is notifying customers about a Data Sheet update for the NCP1250/1 device. Lower limit of lcc Latch current will be split into two temp range rows and adjusted according to latest qual data from new device options

From:

ICCLATCH, Current flowing into VCC pin that keeps the controller latched

30uA

To:

ICCLATCH, Current flowing into VCC pin that keeps the controller latched, Tj -40 °C to 125 °C 40uA ICCLATCH, Current flowing into VCC pin that keeps the controller latched, Tj 0 °C to 125 °C 32uA

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List of affected General Parts:

NCP1250ASN65T1G

NCP1250BSN65T1G

NCP1250ASN100T1G

NCP1250BSN100T1G

NCP1251ASN65T1G

NCP1251BSN65T1G

NCP1251ASN100T1G

NCP1251BSN100T1G

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