

Final Product/Process Change Notification Document #: FPCN21656X

Issue Date: 28 February 2017

Title of Change:	NCP81233MNTXG PMBUS Enable Logic Change from EN=Low to EN=High	
Proposed first ship date:	31 May 2017 or earlier upon customer approval	
Contact information:	Contact your local ON Semiconductor Sales Office or <joe.chong@onsemi.com></joe.chong@onsemi.com>	
Samples:	Contact your local ON Semiconductor Sales Office	
Additional Reliability Data:	Contact your local ON Semiconductor Sales Office or <tomas.vajter@onsemi.com>.</tomas.vajter@onsemi.com>	
Type of notification:	This is a Final Product/Process Change Notification (FPCN) sent to customers. FPCNs are issued 90 days prior to implementation of the change. ON Semiconductor will consider this change accepted, unless an inquiry is made in writing within 30 days of delivery of this notice. To do so, contact <pcn.support@onsemi.com>.</pcn.support@onsemi.com>	
Change Part Identification:	Affected products will be identified with date code	
Change category:	☐ Wafer Fab Change ☐ Asse	mbly Change Test Change Other
Change Sub-Category(s): ☐ Manufacturing Site Change/Addition ☐ Manufacturing Process Change ☐ Product specific change ☐ Other:		
Sites Affected: All site(s) ON Semiconductor site(s): External Foundry/Subcon site(s)		
Description and Purpose:		
The purpose of this Final PCN is to notify customers of the change in PMBUS enable logic to enable/disable NCP81233MNTXG. EN pin should be on High state to allow user to enable/disable NCP81233MNTXG thru PMBUS.		
Please see datasheet for detailed information: http://www.onsemi.com/pub_link/Collateral/NCP81233-D.PDF		
Reliability Data Summary:		
No reliability testing required for this change.		
Electrical Characteristic Summary:		
Electrical characteristics are not impacted		
List of affected Standard Parts:		
Part Number		Qualification Vehicle
NCP81233MNTXG		NCP81233MNTXG

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