



FINAL PRODUCT/PROCESS CHANGE NOTIFICATION #16905Generic Copy

Issue Date: 11-Sep-2012**TITLE:** PCWB and PCWC spec relaxation for the linearity measurement of the ADC (ADC DNL)**PROPOSED FIRST SHIP DATE:** 01-Jan-2013**AFFECTED CHANGE CATEGORY(S):** Final test / spec update**FOR ANY QUESTIONS CONCERNING THIS NOTIFICATION:**

Contact your local ON Semiconductor Sales Office or <filip.thierens@onsemi.com>

SAMPLES: Contact your local ON Semiconductor Sales Office**NOTIFICATION TYPE:**

Final Product/Process Change Notification (FPCN)

Final change notification sent to customers. FPCNs are issued at least 90 days prior to implementation of the change.

ON Semiconductor will consider this change approved unless specific conditions of acceptance are provided in writing within 30 days of receipt of this notice. To do so, contact <quality@onsemi.com>.

DESCRIPTION AND PURPOSE:

- PCWB spec update from version 1.4 to version 1.5
- PCWC spec update from version 1.0 to version 1.1

Spec update:

Addition of specification parameter: defining parameter 4.3 ADCdnl for temperatures $T_j > 125^\circ\text{C}$: relaxation of limits to $\pm 1\text{lsb}$.**Actual specification :**

| Ref Nr. | Symbol | Description | Min | Typ | Max | Unit | Condition |
|---------|---------|-----------------------|------|-----|-----|---------|-----------------------------|
| 4.1 | ADCres | ADC resolution | | 8 | | Bits | For info only |
| 4.2 | ADCref | ADC reference voltage | | 1.2 | | V | For info only |
| 4.3 | ADCdnl | ADC DNL | -0.5 | | 0.5 | LSB | Tested for ADC fs = 3V |
| 4.4 | ADCinl | ADC INL | -1 | | 1 | LSB | Tested for ADC fs = 3V |
| 4.5 | ADCconv | ADC conversion time. | | 15 | | Tadcclk | Tadcclk = 0.5 μs |


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Proposed new specification:

| Ref Nr. | Symbol | Description | Min | Typ | Max | Unit | Condition |
|---------|---------|-----------------------|------|-----|-----|---------|---|
| 4.1 | ADCres | ADC resolution | | 8 | | Bits | For info only |
| 4.2 | ADCref | ADC reference voltage | | 1.2 | | V | For info only |
| 4.3 | ADCdnl | ADC DNL | -0.5 | | 0.5 | LSB | Tested for ADC fs = 3V |
| | | | -1 | | 1 | LSB | Tested for ADC fs = 3V Tj > 125C. Based on characterization. Only tested at max Tj in production. |
| 4.4 | ADCinl | ADC INL | -1 | | 1 | LSB | Tested for ADC fs = 3V |
| 4.5 | ADCconv | ADC conversion time. | | 15 | | Tadcclk | Tadcclk = 0.5μs |

List of affected Customer Specific Parts:

0PCWB-002-XTP
 0PCWC-002-XTP
 0PCWC-003-XTP
 0PCWC-004-XTP
 0PCWC-006-XTP
 0PCWC-007-XTP
 0PCWC-008-XTP