

Product Bulletin

Document #: PB21697Z Issue Date: 13 April 2017

Title of Change:	NCV887x Family Datasheet Update
Effective date:	13 April 2017
Contact information:	Contact your local ON Semiconductor Sales Office or bill.fontes@onsemi.com>
Type of notification:	ON Semiconductor will consider this change accepted.
Change category:	☐ Wafer Fab Change ☐ Assembly Change ☐ Test Change ☐ Other: <u>Datasheet Update</u>
Change Sub-Category(s): Manufacturing Site Chan Manufacturing Process C	
Sites Affected: ☐ All site(s)	applicable
Description and Purpose:	
It has been discovered that the device may become locked under certain conditions. In order to help customers avoid the problem, the datasheet has been updated as follows on page 8 (new text is highlighted): UVLO Input Undervoltage Lockout (UVLO) is provided to ensure that unexpected behavior does not occur when VIN is too low to support the internal rails and power the controller. The IC will start up when enabled and VIN surpasses the UVLO threshold plus the UVLO hysteresis and will shut down when VIN drops below the UVLO threshold or the part is disabled. To avoid any lock state under UVLO conditions, the EN/SYNC pin should be in logic—low state. For further details, please refer to EN/SYNC paragraph.	
List of affected Standard Parts: NCV887000D1R2G NCV887100D1R2G NCV887101D1R2G NCV887102D1R2G NCV887103D1R2G NCV887103D1R2G NCV887104D1R2G NCV887104D1R2G NCV887300D1R2G NCV887300D1R2G NCV887301D1R2G NCV887301D1R2G	

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