



<b>Title of Change:</b>	NCV7535 Datasheet Update	
<b>Effective date:</b>	14 February 2018	
<b>Contact information:</b>	Contact your local ON Semiconductor Sales Office or <Nixon.Mathew@onsemi.com>	
<b>Type of notification:</b>	This Product Bulletin is for notification purposes only. ON Semiconductor will proceed with implementation of this change upon publication of this Product Bulletin.	
<b>Change category:</b>	<input type="checkbox"/> Wafer Fab Change <input type="checkbox"/> Assembly Change <input type="checkbox"/> Test Change <input checked="" type="checkbox"/> Other: <u>Datasheet update</u>	
<b>Change Sub-Category(s):</b>	<input type="checkbox"/> Manufacturing Site Change/Addition <input type="checkbox"/> Material Change <input checked="" type="checkbox"/> Datasheet/Product Doc change <input type="checkbox"/> Manufacturing Process Change <input type="checkbox"/> Product specific change <input type="checkbox"/> Shipping/Packaging/Marking <input type="checkbox"/> Other: _____	
<b>Sites Affected:</b>	ON Semiconductor Sites: None	External Foundry/Subcon Sites: None
<b>Description and Purpose:</b>  <b>Features list updated</b> <ul style="list-style-type: none"> <li>“AEC-Q100 Qualified and PPAP Capable” sentence added</li> </ul> <b>Absolute Maximum Ratings updated</b> <ul style="list-style-type: none"> <li>Vmax_digIO Max limits updated for EN and CSN pins from “VCC+0.3 V” to “5.8 V” -&gt; Maximum value not changed, only different interpretation</li> <li>Iinj_digIO parameter added</li> </ul> <b>SPI Registers descriptions updated</b> <ul style="list-style-type: none"> <li>GLOBAL STATUS BYTE CONTENT – NRDY bit description extended (“This bit indicates that the driver cannot be activated and the chargepump is switched off.”)</li> <li>STATUS_0.VSUV description updated (note that this bit is latched was added)</li> <li>STATUS_0.VSOV description updated (note that this bit is latched was added)</li> </ul> <b>Editorial corrections</b> <ul style="list-style-type: none"> <li>Typos in Figure 7 fixed (incorrect numbers of the data bits)</li> <li>Typos in CONFIG register (page 17) fixed (incorrect bit names)</li> </ul> <p>The change will not impact form, fit, or function of product.</p>		
<b>List of Affected Standard Parts:</b>  NCV7535DBR2G		