ON Semiconductor®



Final Product/Process Change Notification Document # : FPCN22185X

Issue Date: 16 October 2018

Title of Change:	Former Fairchild Tiny Logic [®] MicroPak 8lds Die and Back End Material Change, Assembly and Test Transfer and Datasheet Change.									
Proposed first ship date:	23 January 2019									
Contact information:	Contact your local ON Semiconductor Sales Office or < <u>logic.fpcn@onsemi.com</u> >									
Samples:	Contact your local ON Semiconductor Sales Office of Sample requests are to be submitted no later than 3 PCN, for this change.	r < <u>PCN.samples@onsemi.com</u> > 30 days from the date of first notification, Initial PCN or Final								
Additional Reliability Data:	Contact your local ON Semiconductor Sales Office or	r < <u>ChangKit.Mok@onsemi.com</u> >.								
Type of notification:	delivery of this notice. To do so, contact < <u>PCN.Support@onsemi.com></u>									
Change Part Identification:	Affected product will be marked with new plant coo	Je								
Change Category:	✓ Wafer Fab Change ✓ Assembly Change	▼ Test Change								
Change Sub-Category(s): Manufacturing Site Active Manufacturing Site Tra Manufacturing Process	ansfer Product specific change	 Datasheet/Product Doc change Shipping/Packaging/Marking Other: 								
Sites Affected:	ON Semiconductor Sites: ON S. Portland, Maine	External Foundry/Subcon Sites: Subcon Thailand External Foundry Japan								
Description and Purpose:										
Qualify new die source for Forr	ner Fairchild Tiny Logic® and transfer to a new subcon s	ite to increase capacity.								
Material to be changed	Before Change (Existing flow)	After Change (new flow)								
Assy Site	Subcon Thailand	Subcon Thailand								
Mold Compound	MC CEL9220HF13H HF	MOLDING COMPOUND; G700LTD								
Wire	Au	PCC								
Lead Frame	LF UQFN 8L C7025 Cu 1.6X1.6 ETCHED UPPF	LF PPF+RT-UPG								
Die Attach	DA EPOXY HE ABLEBOND 8006NS 10CC 14G NON CON	NON-CONDUCTIVE DIE ATTACH FILM; HR-5104								
Die Source	Onsemi Fab in Maine US	Foundry in Japan								



Dires. The new datasheet will b xisting datasheet Power Down High-Impedance Inputs/Outputs Over-Voltage Tolerance Inputs Facilitate 5V to 3V Translation	MicroPak MLP/ Micro MLP Top And Micro Pak 2 Top Mark Layout 12KK 	MicroPak MLP/ Micro MLP Top And Micro Pak 2 Top Mark Layout 12KK L L L L L L L L L L L L L L L L L L L
es. The new datasheet will b sting datasheet Power Down High-Impedance Inputs/Outputs Over-Voltage Tolerance Inputs Facilitate 5V to 3V Translation		com customer web site for comparison purposes until the
	155V to 55V Voo The inputs and outputs are high impedance when Voo is OV. Inputs tolerate voltages up	Power Down High-Impedance Inputs/Outputs Over-Voltage Tolerance Inputs Facilitate 5V to 3V Translation
Proprietary Noise/EM Reduction Circuitry Ultra-Small MicroPak™ Package Space-Saving US8 Surface Mount Package	the U output during the positive-going transition of the CLK pulse.	Proprietary Noise/EMI Reduction Circuitry The signal level applied to the D input is transf the Q output during the positive-going transitio CLK pulse.
Vcc Supply Voltage	-0.5 7.0 V	Vcc Supply Voltage -0.5 6.5
V _{IN} DC Input Voltage		V _{IN} DC Input Voltage -0.5 6.5
Vour DC Output Voitage		V 0.0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
Input Leakage 0 to 5.5 0 ≤ V _{III} ≤ 5.5 V	-0.5 7.0 V	Vour DC Output Vottage -0.5 6.5



Final Product/Process Change Notification Document #: FPCN22185X

Issue Date: 16 October 2018

C.m.h.el	Decementer	Vcc (V)		Conditions		T_8=+25°C				T _r =-40	Units		
Symbol	Parameter					Min.	Тур.	Ma	۰.	Min.	Max.	Units	
		1.65	+		+	0.60	1.00	1.40	5	0.60	1.40	-	
		1.80	+		+	0.70	1.10	1.50	5	0.70	1.50	1	
	Positive Threshold	2.30	+		+	1.00	1.40	1.80	, 	1.00	1.80	1	
V,	Voltage	3.00	+		+	1.30	1.75	2.20		1.30	2.20	1	
		4.50	+		+	1.90	2.45	3.10	, 	1.90	3.10	1	
		5.50	+		+	2.20	2.90	3.60		2.20	3.60	1	
		1.65	+		_	0.20	0.50	0.80		0.20	0.80	+	
		1.80	+		_	0.25	0.55	0.90	-	0.25	0.90	1	
	Negative Threshold	2.30	+		_	0.40	0.75	1.1	-	0.40	1.15	- v	
٧x	Voltage	3.00	+		_	0.60	1.00	1.50	_	0.60	1.50		
		4.50	+		+	1.00	1.43	2.00	5	1.00	2.00	-	
		5.50	+		+	1.20	1.70	2.30		1.20	2.30	-	
			-		-				-			-	
		Vcc		$T_A = +25^{\circ}C$		T _A = -40°C to +85°C Min Max		85°C	Units			Figure	
Symbol	Parameter	(V)	Min	Typ /	Aax			Max		Conditions		Number	
¢цн.	Propagation Delay	1.8±0.15	2.0		2.0	2.0	0 13.0			C _L = 15 pF			
\$νHL	A _N to Y _N	2.5 ± 0.2	1.0		7.5	1.0) 8	3.0	ns	$R_{D} = 1$		Figures 1, 3	
		3.3±0.3	0.8		5.2	0.8		5.5	119	S1= Op	en		
		5.0 ± 0.5	0.5		4.5	0.5	5 4	1.8					
ΨLH,	Propagation Delay	3.3±0.3	1.2		5.7	1.2	1.2 6.0			$C_{L} = 50$	C _L = 50 pF		
ФнL	A _N to Y _N	5.0±0.5	0.8		5.0	0.8	3 6	5.3	ns	$R_0 = 50$	10Ω	Figures 1, 3	
										S1= Open			
toslн.	Output to Output Skew	3.3±0.3			1.0			1.0		C _L = 50		Figures	
toshi.	(Note 5)	5.0±0.5			8.0		0	0.8 n		$R_D = 50$		1, 3	
										S1= Op			
¢π.	Output Enable Time	1.8±0.15	3.0		4.0	3.0		5.0	C _L = 5				
∳дн		2.5±0.2	1.8		8.5	1.8		9.0			= 500 Ω		
		3.3±0.3	1.2		6.2	1.2		5.5	ns		ND for t _{P ZH}		
		5.5±0.5	0.8		5.5	0.8	3 6	5.8			for to ZL	1,3	
										$V_1 = 2 \times$			
	Output Disable Time	1.8±0.15	2.5	1	2.0	2.5	j 1	3.0	C _L = 50) pF			
φ _{LZ} ,					8.0	1.5	5 8	3.5		Ro. Ru	= 500 Q		
ФLZ. ФНZ		2.5±0.2	1.5										
ΦLZ- ΦHZ		2.5±0.2 3.3±0.3	1.5		5.7	0.8	3 6	5.0	ns	S1 = G	ND for to zH	Figures	
								5.0 5.0	ns		ND for to ZH for to ZL	Figures 1, 3	

C				C			T _A =+25°C	2	T _A =-40 t	o +85°C	
Symbo	Parameter	Vcc	(V)	Conditions		Min.	Тур.	Max.	Min.	Max.	Units
		1.6	55			1	1.00	1.40		1.40	\square
		1.8	30				1.10	1.50		1.50	1
	Positive Threshold Voltage	2.3	30			-	1.40	1.80		1.80	1
٧,		3.0	0				1.75	2.20		2.20	1
		4.5	50				2.45	3.10	+ +	3.10	1
		5.5					2.90	3.60	+ +	3.60	1
		1.6				0.20	0.50	0.00	0.20	0.00	
		1.8				0.25	0.55		0.25		1
	Negative Threshold Voltage	2.3				0.40	0.75		0.40		-
V.						0.60	1.00		0.60		l v
		3.0									4
		4.5				1.00	1.43		1.00		1
		5.5	50			1.20	1.70		1.20		
										1	_
Symbol	Parameter	Vcc	Min	T _A = +25°C		T _A = -40°C to +85°C Min Max		Units	Conditions		jure nber
	Propagation Delay	(V) 1.8±0.15	Min	Тур	Max 12.0	Min	13.0	-	C1 = 15 pF	Num	per
PLH.	An to Yn	2.5±0.2			7.5		13.0	+	CL=15pr Rp=1 MΩ	E 100	
PHL A	, o, u	3.3±0.3			5.2	-	5.5	ns	S1=Open	Figu	10 S
		5.0±0.5			4.5	-	4.8	+	or = open		
tpln,	Propagation Delay	3.3±0.3			5.7		6.0	-	C _L = 50 pF		-
tPHL	Anto Yn	5.0±0.5			5.0	5.3		ns	R _D = 500Ω	Figu	
									S1=Open	1,3	°
tosun.	Output to Output Skew	3.3±0.3			1.0		1.0	-	C _L = 50 pF	-	-
toshL	(Note 5)	5.0±0.5			0.8		0.8	ns	R _D = 500Ω	Figu 1,	
									S1=Open		
tpzL.	Output Enable Time	1.8±0.15			14.0		15.0		C _L = 50 pF		
трун		2.5±0.2			8.5		9.0		R _D , R _U = 500		
		3.3±0.3			6.2		6.5	ns	S1 = GND for	12H 1.1	3
		5.5±0.5			5.5		5.8		$S1 = V_1$ for t_{PZ}	L	
									$V_1 = 2 \times V_{CC}$		
PLZ.	Output Disable Time	1.8±0.15			12.0		13.0		C _L = 50 pF		
		2.5±0.2			8.0	1	8.5		R _D , R _U = 500		
		3.3±0.3			5.7		6.0	ns	S1 = GND for		3
tpi/Z		3.3±0.3 5.0±0.5			5.7 4.7		6.0 5.0	ns	S1 = GND for S1 = V_1 for t_{PZ} V_1 = 2 × V_{CC}	1, PER	3

Reliability Data Summary:

DEVICE: NC7SZ74L8X

RMS: W45201

PACKAGE: UQFN8

Test	Specification	Condition	Interval	Results
HTOL	JESD22-A108	Ta=125°C, Vcc = 6.6V (1.2X of Vcc max)	1008 hours	0/240
HTSL	JESD22-A103	Ta= 150°C	1008 hours	0/240
PC	J-STD-020 JESD-A113	MSL 1@260°C	-	0/720
TC + PC	JESD22-A104	Ta= -65°C to +150°C	500 cycles	0/240
HAST + PC	JESD22-A110	130°C, 85% RH, 18.8psig, bias, Vcc=5.5V	192 hours	0/240
uHAST + PC	JESD22-A118	130°C, 85% RH, 18.8psig, unbiased	96 hours	0/240
RSH	JESD22- B106	Ta = 265C, 10 sec	-	0/90
SD	JSTD002	Ta = 245C, 10 sec	-	0/30



Electrical Characteristic Summary:						
Electrical characteristics Available upon request.						
List of Affected Parts:						
Part Number	Qualification Vehicle					
NC7SZ74L8X						
NC7NZ34L8X]					
NC7WZ125L8X]					
NC7WZ86L8X]					
NC7WZ126L8X						
NC7NZ17L8X]					
NC7WZ02L8X						
NC7NZ04L8X	NC7SZ74L8X					
NC7WZ08L8X	NC/32/4L6A					
NC7WZ241L8X						
NC7WZ00L8X						
NC7NZ14L8X						
NC7WZ240L8X						
NC7WZ32L8X						
NC7WZ38L8X						
NC7WZ132L8X						