


Old iBGA Package Pinout (Figure 66) with incorrect column 10 label

BALL A1 ID		1	2	3	4	5	6	7	8	10
A		P1_DATA_P	DATA_P	DATA2_P	CLK_N	DATA3_N	DATA4_P	GPIO0	CLK_EN	CS_BAR
B		P1_DATA_N	DATA_N	DATA2_N	CLK_P	DATA3_P	DATA4_N	TEST	GPIO3	SHUTDOWN
C		P1_DATA2_P	P1_DATA2_N	DIAGN_TX				Sense	GPIO1	GPIO2
D		P1_CLK_N	P1_CLK_P					Sense	ATEST1	
E		P1_DATA3_N	P1_DATA3_P					VDD_F0K	Acqnt_F0K	
F		P1_DATA4_P	P1_DATA4_N					VDD	VDD	
G		EXTCLK	DIAGN_TX	VDD				Acqnt	Acqnt	VDD
H		VDD_I0B	VDD_TX	VDD_ID	VDD_ID	DIAGN	VDD	VDD_ID	Acqnt	VDD
J		VDD	VDD	DIAGN	DIAGN	DIAGN	VDD	VDD	VDD	Acqnt

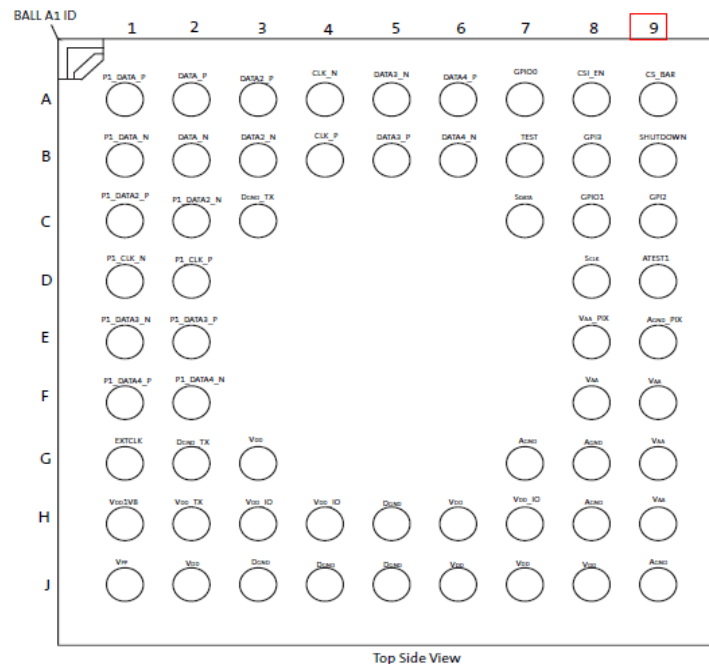
Top Side View



New iBGA Package Pinout (Figure 66) with column 10 correctly changed to column 9

Package Dimensions

Figure 66: iBGA Package Pinout

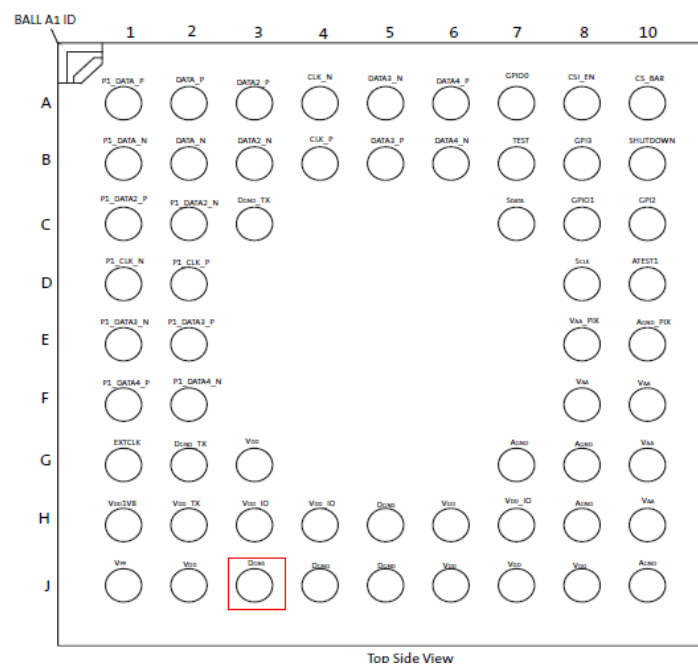


2. Change solder ball J3 from "DGNS" to "DGND"

Old iBGA Package Pinout (Figure 66) with J3 incorrectly labeled as DGNS

Package Dimensions

Figure 66: iBGA Package Pinout

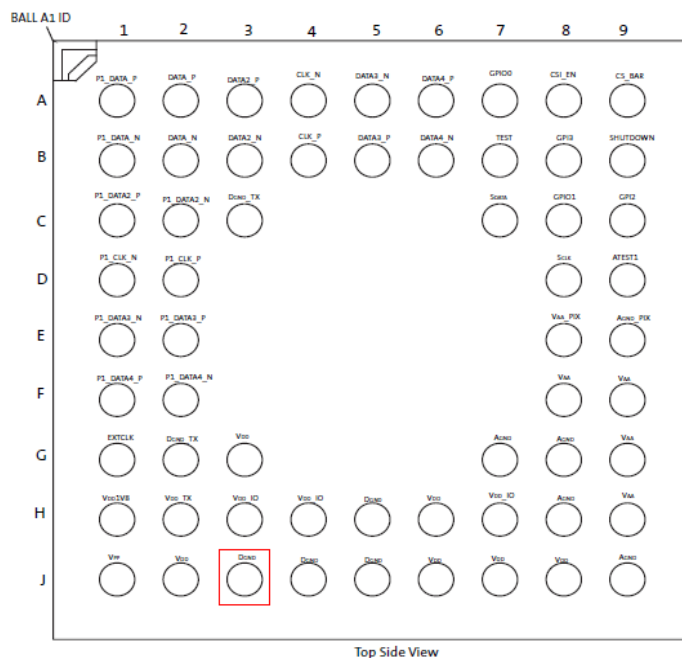




New iBGA Package Pinout (Figure 66) with J3 correctly labeled as DGND

Package Dimensions

Figure 66: iBGA Package Pinout



This change is the result of a documentation error only; there is no change to the product form, fit, or function.

List of affected Standard Parts:

AR1820HSSC00SHEA0-DR
 AR1820HSSC12SHEA0-DR
 AR1820HSSC12SMD20
 AR1820HSSC31SMD20
 AR1820HSSC00SHQAD3-GEVK
 AR1820HSSC00SHQAH3-GEVB
 AR1820HSSC12SHQAD3-GEVK
 AR1820HSSC12SHQAH3-GEVB
 AR1820HSSC31SMEAD3-GEVK
 AR1820HSSC31SMEAH3-GEVB