



Title of Change:	Ezairo 7110: increased maximum system clock.	
Effective Date:	Effective Immediately	
Contact information:	Contact your local ON Semiconductor Sales Office or <Christophe.waelchli@onsemi.com>	
Type of notification:	ON Semiconductor considers this change approved unless specific conditions of acceptance are provided in writing. To do so, contact <PCN.Support@onsemi.com>.	
Change category(s): <input type="checkbox"/> Wafer Fab Change <input type="checkbox"/> Assembly Change <input checked="" type="checkbox"/> Test Change	<input type="checkbox"/> Manufacturing Site Change/Addition <input type="checkbox"/> Manufacturing Process Change <input type="checkbox"/> Material Change	<input type="checkbox"/> Product specific change <input type="checkbox"/> Datasheet/Product Doc change <input type="checkbox"/> Shipping/Packaging/Marking <input type="checkbox"/> Other: _____
Sites Affected: <input type="checkbox"/> All site(s) <input type="checkbox"/> not applicable <input type="checkbox"/> ON Semiconductor site(s) : <input checked="" type="checkbox"/> External Foundry/Subcon site(s):	<u>Site 1</u> Ardentec	<u>Site 2</u>
Description and Purpose: The maximum available system clock frequency of the Ezairo 7100 DSP has been increased from 10.24MHz to 15.36MHz. The Ezairo 7110 hybrid is using the Ezairo 7100 DSP. Hence, the maximum available system clock frequency for the hybrid is increased in a similar way. The new datasheet version of the Ezairo 7110 hybrid is Rev 9. Parts with work order 504226 and later will support the new maximum system clock.		
List of affected Standard Parts: E7110-102A33-AG		