

# Final Product/Process Change Notification Document #:FPCN25718X

Document #:FPCN25718X Issue Date:06 Nov 2023

Title of Change:	NCD57101 revised design for improved performance.		
Proposed First Ship date:	05 Feb 2024 or earlier if approved by customer		
Contact Information:	Contact your local onsemi Sales Office or <a href="mailto:David.Craig@onsemi.com">David.Craig@onsemi.com</a>		
PCN Samples Contact:	Contact your local onsemi Sales Office. Sample requests are to be submitted no later than 30 days from the date of first notification, Initial PCN or Final PCN, for this change. Samples delivery timing will be subject to request date, sample quantity and special customer packing/label requirements.		
Additional Reliability Data:	Contact your local onsemi Sales Office or Nicky.Siu@onsemi.com		
Type of Notification:	This is a Final Product/Process Change Notification (FPCN) sent to customers. FPCNs are issued 90 days prior to implementation of the change. onsemi will consider this change accepted, unless an inquiry is made in writing within 30 days of delivery of this notice. To do so, contact <a href="PCN.Support@onsemi.com">PCN.Support@onsemi.com</a>		
Marking of Parts/ Traceability of Change:	Changed material can be identified by lot code.		
Change Category:	Design Change		
Change Sub-Category(s):	Product specific change		
Sites Affected:			
onsemi Sites		External Foundry/Subcon Sites	
None		None	

### **Description and Purpose:**

This final change notification wants to inform the customer regarding NCD57101 redesign to improve product performance and extend the differential power supply limit to 36V.

	From	То	
Data sheet	Datasheet Rev. P0 (dated May, 2022) with errata	Revised datasheet without errata	
Other Changes	Die revision: G0B07DJ-Y0XT	Die revision: G0D07DJ-Y0XT	

There is no product marking change as a result of this change.

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#### **Reliability Data Summary:**

QV DEVICE NAME: NCV57100DWR2G

RMS: O86181 PACKAGE: SOIC16WB

Test	Specification	Condition	Interval	Results
High Temperature Operating Life	JESD22-A108	Ta= 125 °C, 100 % max rated Vcc	1008 hrs	0/294
High temperature Blocking Bias	MIL STD 750 Method 1048 para 3	Ta= 125 °C, VISO = 1200V	1008 hrs	0/239
High Voltage Temperature Humidity Bias	NA	Ta= 85 °C, RH = 60%, VISO = 1200V	168 hrs	0/240

#### **Electrical Characteristics Summary:**

Elimination of errata, which limited minimum pulse width and power supply ranges.

#### Before change:

 $t_{MIN2}$ , Minimum = 500ns  $V_{EE2} - GND2$ , Maximum = -2V  $V_{DD2} - V_{EE2}$ , maximum = 26V

#### After change:

 $t_{MIN2}$ , Minimum = 40ns  $V_{EE2}$  – GND2, Maximum = 0.3V  $V_{DD2}$ – $V_{EE2}$ , maximum = 36V

#### **List of Affected Parts:**

**Note:** Only the standard (off the shelf) part numbers are listed in the parts list. Any custom parts affected by this PCN are shown in the customer specific PCN addendum in the PCN email notification, or on the **PCN Customized Portal**.

Part Number	Qualification Vehicle
NCD57101DWR2G	NCV57101DWR2G

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