



## PRODUCT BULLETIN # 16505

**Issue Date:** 17-Aug-2010

**TITLE:** NCV8184 Datasheet Change (Current Limit Spec)

**PROPOSED FIRST SHIP DATE:** 17-Aug-2010

**AFFECTED CHANGE CATEGORY(S):** Datasheet Only

**FOR ANY QUESTIONS CONCERNING THIS NOTIFICATION:**

Contact your local ON Semiconductor sales office or Bill Fontes <[Bill.Fontes@onsemi.com](mailto:Bill.Fontes@onsemi.com)>

**NOTIFICATION TYPE:**

ON Semiconductor considers this change approved unless specific conditions of acceptance are provided in writing. To do so, contact <[quality@onsemi.com](mailto:quality@onsemi.com)>.

**DESCRIPTION AND PURPOSE:**

For the parameter "Current Limit", the maximum limit will be lowered from 400mA to 225mA.

**ELECTRICAL CHARACTERISTICS** ( $V_{IN} = 14\text{ V}$ ;  $V_{REF/ENABLE} > 2.1\text{ V}$ ;  $-40^{\circ}\text{C} < T_J < +150^{\circ}\text{C}$ ;  $C_{OUT} = 1.0\text{ }\mu\text{F}$ ;  $I_{OUT} = 1.0\text{ mA}$ ;  $\text{Adj} = V_{OUT}$ ;  $C_{OUT-ESR} = 1.0\text{ }\Omega$ , unless otherwise specified.)

Parameter	Test Conditions	Min	Typ	Max	Unit
<b>REGULATOR OUTPUT</b>					
$V_{REF/ENABLE} - V_{OUT}$ V <sub>OUT</sub> Tracking Error	$5.7\text{ V} \leq V_{IN} \leq 26\text{ V}$ , $100\text{ }\mu\text{A} \leq I_{OUT} \leq 60\text{ mA}$ $2.1\text{ V} \leq V_{REF/ENABLE} \leq (V_{IN} - 600\text{ mV})$	-3.0	-	3.0	mV
Dropout Voltage ( $V_{IN} - V_{OUT}$ )	$I_{OUT} = 100\text{ }\mu\text{A}$ $I_{OUT} = 5.0\text{ mA}$ $I_{OUT} = 60\text{ mA}$	-	100 250 350	150 500 600	mV mV mV
Line Regulation	$5.7\text{ V} \leq V_{IN} \leq 26\text{ V}$ , $V_{REF/ENABLE} = 5.0\text{ V}$	-	-	3.0	mV
Load Regulation	$100\text{ }\mu\text{A} \leq I_{OUT} \leq 60\text{ mA}$ , $V_{REF/ENABLE} = 5.0\text{ V}$	-	-	3.0	mV
Adj Input Bias Current	$V_{REF/ENABLE} = 5.0\text{ V}$	-	0.2	6.0	$\mu\text{A}$
Current Limit	$V_{IN} = 14\text{ V}$ , $V_{REF} = 5.0\text{ V}$ , $V_{OUT} = 90\%$ of $V_{REF}$ (Note 3)	70	-	225	mA
Quiescent Current ( $I_{IN} - I_{OUT}$ )	$V_{IN} = 12\text{ V}$ , $I_{OUT} = 60\text{ mA}$ $V_{IN} = 12\text{ V}$ , $I_{OUT} = 100\text{ }\mu\text{A}$ $V_{IN} = 12\text{ V}$ , $V_{REF/ENABLE} = 0\text{ V}$	-	5.0 50 -	7.0 70 20	mA $\mu\text{A}$ $\mu\text{A}$
Ripple Rejection	$f = 120\text{ Hz}$ , $I_{OUT} = 60\text{ mA}$ , $6.0\text{ V} \leq V_{IN} \leq 26\text{ V}$	60	-	-	dB
Thermal Shutdown	Guaranteed by Design	150	180	210	$^{\circ}\text{C}$
<b>V<sub>REF/ENABLE</sub></b>					
Enable Voltage	-	0.8	-	2.1	V
Input Bias Current	$V_{REF/ENABLE} = 5.0\text{ V}$	-	0.2	3.0	$\mu\text{A}$

3.  $V_{OUT}$  connected to Adj lead.

The purpose of this change is to align the datasheet more closely with the device capability. Manufacturing data has shown that the NCV8184 is easily capable of meeting this tighter specification.



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**List of Affected Parts:**

NCV8184PDG  
NCV8184PDR2G  
NCV8184DG  
NCV8184DR2G  
NCV8184DTRKG