

Title of Change:	NCV47722 datasheet update				
Effective date:	26 Jun 2020				
Contact information:	Contact your local ON Semiconductor Sales Office or Juraj.Kremmer@onsemi.com				
Type of notification:	This Product Bulletin is for notification purposes only. ON Semiconductor will proceed with implementation of this change upon publication of this Product Bulletin.				
Change Category:	Datasheet/Product Doc change				
Change Sub-Category(s):	Datasheet/Product Doc change				
Sites Affected:					
ON Semiconductor Sites		External Foundry/Subcon Sites			
None		None			
Description and Purpose:					

- 1) Removing package information of those packages marked as "In Development" on Page 1: SO-8, SO-8EP and DFN8. These were meant as potential future package options, however there is no actual development work ongoing. This change is done to avoid confusions. Changes on Page 1, 3, 10, 12, 13, 14.
- 2) There are 3 minor corrections done in the datasheet to avoid confusions. Changes on Page 2, 7 and 10.

Changes to be implemented in datasheet revision: NCV47722/D, July, 2020 – Rev. 1.

The changes will not impact form, fit, or function of product.

1) Removing package information of those packages marked as "In Development".

Page 1: Removing SO-8, SO-8EP and DFN8 packages from the Marking Diagram section

Page 3: Removing SO-8, SO-8EP and DFN8 Pin connections diagrams in Figure 3. And columns belonging to SO-8, SO8EP and DFN8 Pin numbering from Table 1.





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Page 12,13,14: Removing SO-8, SO-8EP and DFN8 case outlines.

2) Minor corrections in the datasheet

## Page 2: Correcting VREF\_OFF from 1.05V to 0.5V

## Page 7: Correcting the definition of the Current Limit

From: Current Limit is value of output current by which output voltage drops below 90% of its nominal value. To: Current Limit is value of output current by which output voltage drops of 1V below input supply voltage level.





Page 10: Besides removing package data, also removing "Output Voltage" definition from the ORDERING INFORMATION table.						
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