



PCN# : P57VAA
Issue Date : Sep. 01, 2015

DESIGN/PROCESS CHANGE NOTIFICATION

This is to inform you that a change is being made to the products listed below.

Unless otherwise indicated in the details of this notification, the identified change will have no impact on product quality, reliability, electrical, visual or mechanical performance and affected products will remain fully compliant to all published specifications. Products incorporating this change may be shipped interchangeably with existing unchanged products.

This change is planned to take effect in 90 calendar days from the date of this notification. Please work with your local Fairchild Sales Representative to manage your inventory of unchanged product if your evaluation of this change will require more than 90 calendar days.

Please contact your local Customer Quality Engineer within 30 days of receipt of this notification if you require any additional data or samples.

Implementation of change:

Expected First Shipment Date for Changed Product : Nov. 30, 2015

Expected First Date Code of Changed Product :154J

Description of Change (From) :

Front-end Wafer Fabrication site Fairchild Bucheon 5-inch wafers

Description of Change (To) :

Front-end Wafer Fabrication Site Hangzhou Silan Foundry 5-inch wafers

Reason for Change:

Improve supply flexibility.

Better quality and yields through equipment and facility upgrades.

- Increased automation in handling and inspection in assembly.

Fairchild partners with foundries and assembly subcontractors.

- Best manufacturing practices, access to many customers methods and practices.

- Advanced technology for fast ramp of future new products and technologies.

Affected Product(s): Please refer to the list of affected products in the addendum attached in the PCN email you received. This list is based on an analysis of your company's procurement history.

Qualification Plan	Device	Package	Process	No. of Lots
Q20150059A	KA7815ETU	TO220	Std Linear	3

Test Description:	Condition:	Standard:	Duration:	Results:
Temperature Humidity Bias Test	85%RH, 85C, 20V	JESD22-A101B	1000 hrs	9/28/2015
High Temperature Op Life	125C, Vbias= 35V	JESD22-A108	1000 hrs	9/28/2015
High Temperature Storage Life	150C	JESD22-A103	1000 hrs	9/28/2015
Temperature Cycle	-55C, 150C	JESD22-A104	1000 cyc	9/5/2015

Qualification Plan	Device	Package	Process	No. of Lots
Q20150059A	KA7915TU	TO220	Std Linear	3

Test Description:	Condition:	Standard:	Duration:	Results:
Temperature Humidity Bias Test	85%RH, 85C, -20V	JESD22-A101B	1000 hrs	9/28/2015
High Temperature Op Life	125C, Vbias= -30V	JESD22-A108	1000 hrs	9/18/2015
High Temperature Storage Life	150C	JESD22-A103	1000 hrs	9/18/2015
Temperature Cycle	-55C, 150C	JESD22-A104	1000 cyc	8/28/2015

Qualification Plan	Device	Package	Process	No. of Lots
Q20150172A	KA431SAMF2TF	NMAE	Std Linear	3

Test Description:	Condition:	Standard:	Duration:	Results:
MSL1 Precondition	260C, 3 cycles	JESD22-A113		9/8/2015
Temperature Humidity Bias Test	85%RH, 85C, 10V	JESD22-A101B	1000 hrs	10/30/2015
High Temperature Op Life	Vcc: 20V, 125C	JESD22-A108	1000 hrs	10/18/2015
High Temperature Storage Life	150C	JESD22-A103	1000 hrs	10/18/2015
Temperature Cycle	-65C, 150C	JESD22-A104	500 cyc	9/24/2015

Qualification Plan	Device	Package	Process	No. of Lots
Q20150172A	LP2951CMX	NMSON	Std Linear	3

Test Description:	Condition:	Standard:	Duration:	Results:
MSL1 Precondition	260C, 3 cycles	JESD22-A113		9/19/2015
Temperature Humidity Bias Test	85%RH, 85C, 10V	JESD22-A101B	1000 hrs	11/11/2015
High Temperature Op Life	Vcc: 30V, 125C	JESD22-A108	1000 hrs	10/29/2015
High Temperature Storage Life	150C	JESD22-A103	1000 hrs	10/29/2015
Temperature Cycle	-65C, 150C	JESD22-A104	500 cyc	10/5/2015