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PCN# : P262A
Issue Date : Jun. 25, 2012

DESIGN/PROCESS CHANGE NOTIFICATION

This is to inform you that a change is being made to the products listed below.

Unless otherwise indicated in the details of this notification, the identified change will have no impact on product quality, reliability, electrical, visual or mechanical performance and affected products will remain fully compliant to all published specifications. Products incorporating this change may be shipped interchangeably with existing unchanged products.

This change is planned to take effect in 90 calendar days from the date of this notification. Please work with your local Fairchild Sales Representative to manage your inventory of unchanged product if your evaluation of this change will require more than 90 calendar days.

Please contact your local Customer Quality Engineer within 30 days of receipt of this notification if you require any additional data or samples. Alternatively, you may send an email request for data, samples or other information to PCNSupport@fairchildsemi.com.

Implementation of change:

Expected First Shipment Date for Changed Product : Sep. 23, 2012

Expected First Date Code of Changed Product :1237

Description of Change (From) :
A summary of the changes is listed below.

Description of Change (To) :

A summary list of the changes is listed below. All changes to FSA9280AUMX_F065 are shown in the table below. Changes that have already been included on the FSA9280AUMX_SN00227 PCN are also noted.

Description of Change (From)	Description of Change (To)	Impact of change
Device will utilize VBUS_IN as its power source when present.	Device updated to place a higher priority on VBAT as the internal power source when both VBAT and VBUS_IN are present.	This change has already been evaluated and is currently been supplied as the FSA9280AUMX_SN00227.
Device is not BC 1.1 compliant as the D+/D- contact is not required.	Device updated to implement USB BC1.1 Data Contact Detect (DCD) including timeout. Timeout required for chargers not meeting BC 1.1 or for long insertion times.	Device is now compliant to USB BC 1.1 specification.
N/A	Added detection (detection only) of audio cradle accessory that uses 365K Ohms on ID for identification.	This change has already been evaluated and is currently been supplied as the FSA9280AUMX_SN00227.
OCP threshold range was 1.5A to 1.9A.	OCP threshold range is 1.1A to 1.5A.	This change has already been evaluated and is currently been supplied as the FSA9280AUMX_SN00227.
Factory Mode resistor tolerance for BOOT OFF UART was +5%/-3%. Factory Mode resistor tolerance for BOOT ON UART was +5%/-5%. Resistor tolerance for CARKIT TYPE 1 was +5%/-5%. Resistor tolerance for CARKIT TYPE 2 was +5%/-5%.	Factory Mode resistor tolerance for BOOT OFF UART is +3%/-3%. Factory Mode resistor tolerance for BOOT ON UART is +3%/-3%. Resistor tolerance for CARKIT TYPE 1 is +3%/-5%. Resistor tolerance for CARKIT TYPE 2 is +3%/-3%.	This change has already been evaluated and is currently been supplied as the FSA9280AUMX_SN00227.
Current fab process was Fairchild's 0.35um CMOS class 1 fab process with 0.35um metallization rules in South Portland, Maine.	Fab process is Fairchild's 0.35um CMOS class 1 fab process utilizing 0.18um metallization rules in South Portland, Maine. Minimum device geometries remain the same.	This change has already been evaluated and is currently been supplied as the FSA9280AUMX_SN00227.

Reason for Change:
Changes will improve quality and applications performance of the device. This change is planned to take effect immediately upon customer approval. Please contact your local Customer Quality Engineer immediately upon receipt of this notification if you require any additional data or samples. Timely approval of this PCN will help to protect your supply chain against any unforeseen supply disruptions.

Affected Product(s):

FSA9280AUMX_F065		

Qualification Plan	Device	Package	Process	No. of Lots
Q20110063	FSA9280SUMX	3x4 20 ld UMX	FS35BCDMOS5_40S	3

Test Description:	Condition:	Standard :	Duration:	Results:
MSL1 Precondition	260C, 3 cycles	**JESD22-A113**		0/276
Highly Accelerated Stress Test	110C, 85%RH, 3.3V	**JESD22-A110**	264 hrs	0/135
High Temperature Storage Life	150C	**JESD22-A103**	1000 hrs	0/231
Static Operation Life	150C, 5.25/3.5V	**JESD22-A108**	1000 hrs	0/231
Temperature Cycle	-65C, 150C	**JESD22-A104**	500 cycles	0/231