## **ON Semiconductor**



Issue Date: 4 May 2015

| •   |  |
|---|--|
| Title of Change:  | Final PCN for wafer fabrication site addition of ON Semiconductor Niigata Co., Ltd. In Niigata, Japan (Group JE)   |
| Proposed first ship date:   | 11 August 2015   |
| Contact information:  | Contact your local ON Semiconductor Sales Office or <yasuhiro.lgarashi@onsemi.com></yasuhiro.lgarashi@onsemi.com>  |
| Samples:  | Contact your local ON Semiconductor Sales Office   |
| Additional Reliability Data:  | Contact your local ON Semiconductor Sales Office or <kazutoshi.kitazume@onsemi.com>.</kazutoshi.kitazume@onsemi.com>   |
| Type of notification:   | This is a Final Product/Process Change Notification (FPCN) sent to customers. FPCNs are issued 90 days prior to implementation of the change.<br>ON Semiconductor will consider this change approved unless specific conditions of acceptance are provided in writing within 30 days of receipt of this notice. To do so, contact <pcn.support@onsemi.com>.</pcn.support@onsemi.com> |
| Change Part Identification:   | Affected products will be identified with date code.   |
| Change category(s):<br>Wafer Fab Change<br>Ssembly Change<br>Test Change  | <ul> <li>Product specific change</li> <li>Manufacturing Site Change/Addition</li> <li>Datasheet/Product Doc change</li> <li>Manufacturing Process Change</li> <li>Shipping/Packaging/Marking</li> <li>Material Change</li> <li>Other:</li> </ul>   |
| Sites Affected:<br>All site(s) not applicable<br>ON Semiconductor site(s) :<br>External Foundry/Subcon site   | ON Niigata, Japan  |
| Description and Purpose:  |  |
| This is a Final Process Change Notification to announce the expanding from conventional Manufacturers UMC to newly wafer fabrication site. Additional fabrication site is ON Semiconductor Niigata Co., Ltd. (OSNC). OSNC is located in Niigata, Japan, obtained ISO9001 certification.   |  |
| The product design and electrical specifications will remain identical. A full electrical characterization over the temperature range will be performed to check the device functionality and electrical specifications. Qualification tests are designed to show that the reliability of transferred devices will continue to meet or exceed ON Semiconductor standards. |  |
| Reliability Data Summary:   |  |
| Test:<br>Steady State Operating Life<br>High Temperature Reverse Bias<br>Temp Humidity Storage<br>Temperature Cycle<br>Pressure Cooker<br>High Temperature Storage<br>Resistance to Soldering heat(Ref<br>Solderability   | Conditions:Interval:ResultsTj=150degC1000 hrsPassTa=150degC,VDSS =max1000 hrsPassTa=85degC, RH=85%1000 hrsPassTa=-55degC to 150degC 30min each100 cyclesPassTa=121degC,2.03×10 $^5$ Pa,100%50 hrsPassTa=150degC1000 hrsPassTa=150degC1000 hrsPassSolder Temp.:260degC±5degC10sPassSolder Temp.: 245degC±5degC5 sPass   |
| Electrical Characteristic Summary:<br>There is no change in the electrical performance. Datasheet specifications remain unchanged.  |  |

## List of Affected Standard Parts:

ATP302-TL-H