

PCN#: P376A

Issue Date : Aug. 16, 2013

DESIGN/PROCESS CHANGE NOTIFICATION

This is to inform you that a change is being made to the products listed below.

Unless otherwise indicated in the details of this notification, the identified change will have no impact on product quality, reliability, electrical, visual or mechanical performance and affected products will remain fully compliant to all published specifications. Products incorporating this change may be shipped interchangeably with existing unchanged products.

This change is planned to take effect in 90 calendar days from the date of this notification. Please work with your local Fairchild Sales Representative to manage your inventory of unchanged product if your evaluation of this change will require more than 90 calendar days.

Please contact your local Customer Quality Engineer within 30 days of receipt of this notification if you require any additional data or samples. Alternatively, you may send an email request for data, samples or other information to PCNSupport@fairchildsemi.com.

Implementation of change:

Expected First Shipment Date for Changed Product: Nov. 14, 2013

Expected First Date Code of Changed Product :1344

Description of Change (From):

Manufacturing source for SC70-6 package located in Jiangyin City, Jiangsu Province, China.

Description of Change (To):

Manufacturing source for SC70-6 package located in Chuzhou City, Anhui Province, China.

BOM comparison table between Jiangyin and Chuzhou site as below:

Process/Material Jiangyin ChuZhou Lead Frame Alloy 42 Alloy 42 Die attach Material Eutectic Eutectic Wire Bonding Material Au Au

Mold Material GE1030S ELER-8-100HFE

Reason for Change:

Fairchild's China Subcontractor moved their manufacturing facility from Jiangyin City to Chuzhou City. The Jiangyin City manufacturing facility has closed the SC70-6 line. Package outline drawings of the affected products remain

unchanged. The affected product will be fully compliant to all the published data sheet specifications and this change will have no impact on their electrical performance. The Quality and reliability will remain at the highest standards already demonstrated with Fairchild's existing product.



Affected	Produc	t(s):
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2N7002DW_G	

Qualification Plan	Device	Package	Process	No. of Lots
QP_1205167D	2N7002DW_G	SC70-6L	Mosfet	3

Test Description:	Condition:	Standard :	Duration:	Results:
MSL1 Precondition	260C, 3 cycles	JESD22-A113		0/693
Temperature Humidity Bias Test	85%RH, 85C, 80% BV	JESD22-A101B	1000 hrs	0/231
High Temperature Reverse Bias Test	150C, 80% BV	JESD22-A108	1000 hrs	0/231
High Temperature Gate Bias Test	150C, 100% of rated Vgs	JESD22-A108	1000 hrs	0/231
High Temperature Storage Life	150C	JESD22-A103	1000 hrs	0/231
Power Cycle	Delta 100C, 2 Min cycle	MIL-STD-750- 1036	10000 cycles	0/231
Temperature Cycle	-65C, 150C	JESD22-A104	500 cycles	0/231