

# **PRODUCT BULLETIN # 16682**

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### Issue Date: 13-Jul-2011

TITLE: NCV4264-2 Datasheet Updates

PROPOSED FIRST SHIP DATE: 13-Oct-2011

AFFECTED CHANGE CATEGORY(S): Datasheet Only

#### FOR ANY QUESTIONS CONCERNING THIS NOTIFICATION:

Contact your local ON Semiconductor sales office or <Peter.Lanyon@onsemi.com>

#### **NOTIFICATION TYPE:**

ON Semiconductor considers this change approved unless specific conditions of acceptance are provided in writing. To do so, contact <quality@onsemi.com>.

#### **DESCRIPTION AND PURPOSE:**

Changes made on page 3 in ELECTRICAL CHARACTERISTICS table of the datasheet of NCV4264–2/D. See below highlighted in red the updated values.

Characteristic	Symbol	Test Conditions	Min	Тур	Max	Unit
Output Voltage 5.0 V Version	V <sub>OUT</sub>	5.0 mA $\leq$ I <sub>OUT</sub> $\leq$ 100 mA (Note 4) 6.0 V $\leq$ V <sub>IN</sub> $\leq$ 28 V	4.900	5.000	5.100	V
Output Voltage 3.3 V Version	V <sub>OUT</sub>	5.0 mA $\leq$ I <sub>OUT</sub> $\leq$ 100 mA (Note 4) 4.5 V $\leq$ V <sub>IN</sub> $\leq$ 28 V	3.234	3.300	3.366	V
Line Regulation 5.0 V Version	ΔV <sub>OUT</sub> vs. V <sub>IN</sub>	$I_{OUT} = 5.0 \text{ mA}$ $6.0 \text{ V} \le \text{V}_{IN} \le 28 \text{ V}$	-30	5.0	+30	mV
Line Regulation 3.3 V Version	ΔV <sub>OUT</sub> vs. V <sub>IN</sub>	$I_{OUT} = 5.0 \text{ mA}$ $4.5 \text{ V} \leq \text{V}_{IN} \leq 28 \text{ V}$	-30	5.0	+30	mV
Load Regulation	ΔV <sub>OUT</sub> vs. I <sub>OUT</sub>	$1.0 \text{ mA} \leq I_{OUT} \leq 100 \text{ mA}$ (Note 4)	-40	5.0	+40	mV
Dropout Voltage - 5.0 V Version	VIN-VOUT	I <sub>OUT</sub> = 100 mA (Notes 4 & 5)	-	270	500	mV
Dropout Voltage – 3.3 V Version	VIN-VOUT	I <sub>OUT</sub> = 100 mA (Notes 4 & 7)	-	-	1.266	V
Quiescent Current	lq	I <sub>OUT</sub> = 100 μA T <sub>J</sub> = 25°C T <sub>J</sub> = -40°C to +85°C T <sub>J</sub> = -40°C to 150°C		33 33 33	55 60 70	μA
Active Ground Current	I <sub>G(ON)</sub>	I <sub>OUT</sub> = 50 mA (Note 4)	-	1.5	4.0	mA
Power Supply Rejection	PSRR	V <sub>RIPPLE</sub> = 0.5 V <sub>P-P</sub> , F = 100 Hz	-	67	-	dB
Output Capacitor for Stability 5.0 V Version	C <sub>OUT</sub> ESR	I <sub>OUT</sub> = 0.1 mA to 100 mA (Notes 4)	10 -	-	_ 9.0	μF Ω
Output Capacitor for Stability 3.3 V Version	C <sub>OUT</sub> ESR	I <sub>OUT</sub> = 0.1 mA to 100 mA (Notes 4)	22	-	- 16	μF Ω

ELECTRICAL CHARACTERISTICS (V<sub>IN</sub> = 13.5 V, T<sub>J</sub> = -40°C to +150°C, unless otherwise noted.)

In addition, the following Vout tests and conditions have been removed:

- Vout at  $5.0\text{mA} \le \text{IOUT} \le 50\text{mA}$  and  $9.0\text{V} \le \text{VIN} \le 16\text{V}$  (for both 5.0 V and 3.3 V version)

Vout at 0 mA  $\leq$  IOUT  $\leq$  100mA and 5.5V  $\leq$  VIN  $\leq$  21V for -40°C  $\leq$  TJ  $\leq$  125°C

The purpose of these changes is to align the datasheet more closely with the device capability. Manufacturing data has shown that the NCV4264–2 is capable of meeting this tighter specification.

# **ON Semiconductor**



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## List of affected General Parts:

NCV4264-2ST50T3G NCV4264-2ST33T3G