ON Semiconductor

PRODUCT BULLETIN # 20248
Generic Copy

Issue Date: 10-Oct-2013

TITLE: LC72715 Datasheet Update

PROPOSED FIRST SHIP DATE: 10-Jan-2014 or sooner on customer approval

AFFECTED CHANGE CATEGORY(S): Datasheet Only

FOR ANY QUESTIONS CONCERNING THIS NOTIFICATION:
Contact your local ON Semiconductor sales office or
Kazuhiro Nakamuta <Kazuhiro.Nakamuta@onsemi.com>

NOTIFICATION TYPE:
ON Semiconductor considers this change approved unless specific conditions of acceptance are provided in writing.   To do so, contact <quality@onsemi.com>.

DESCRIPTION AND PURPOSE:

Several corrections are made with the publication of the revision 1.23 of the LC72715 datasheet.

- Correction of errors:
    - Timing parameter of CPU interface: $tDD\overline{O}$ minimum and maximum time are changed to 135ns and 320ns respectively. (originally 277ns and 555ns respectively)
    - $tDD\overline{O}2$ minimum time is changed to 135ns. (originally 140ns)
    - The explanation of the CPU register "Number of allowable BIC errors": BIC_F (Forward protection value) is changed to bit7-4. (originally bit3-0) and BIC_B (Backward protection value) is changed to bit3-0 (originally bit7-4). BIC_F and BIC_B are replaced for correcting error.
    - The table of "List of operation modes": When "mode 2" is set, the NG data of vertically-corrected output is "not output". (originally "output")

- Additional Information:
    - The name of the VICS Center : The official name "Vehicle Information and Communication System Center" is added.
    - The name of the NHK Engineering Service: The organization name is changed to NHK Engineering System, Inc.
    - The explanation of the CPU register "Control register 1": Additional information of $D\overline{O}$_MOVE is added. Besides the figure of the relationship between INT and $D\overline{O}$ is added.
    - The explanation of the CPU register "Status register" and "Block Number register": Additional information of the value in the "Reset" column is added.
    - The explanation of the register read timing: The condition when the status register can be read in a manner asynchronous with INT is only when INT_MOVE is set to "1".

List of affected General Parts:

LC72717PW-H

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List of affected Customer Specific Parts:

LC72715PW-UONK-TLM-H
LC72715PW-UF-H
LC72715PW-UJK-H
LC72715PW-SH-UD-H
LC72715PW-UP-H
LC72715W-UGAR-TLM-H
LC72715LG-UM-TLM-H
LC72715PW-TLM-H
LC72715LG-TLM-H
LC72715PLG-H
LC72715PW-H