



PRODUCT BULLETIN
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10-Aug-2009

SUBJECT: ON Semiconductor Product Bulletin #16306

TITLE: Optimization of GDLC performance

PROPOSED FIRST SHIP DATE: 30-Sep-2009

AFFECTED PRODUCT DIVISION: Mixed signal Automotive

FOR ANY QUESTIONS CONCERNING THIS NOTIFICATION:

Contact your local ON Semiconductor sales office.

NOTIFICATION TYPE:

ON Semiconductor considers this change approved unless specific conditions of acceptance are provided in writing. To do so, contact your local ON Semiconductor sales office.

DESCRIPTION AND PURPOSE:

ON Semiconductor is notifying its customer about a minor change that will be implemented in GDLC and has a positive effect on the performance of GDLC during OTP programming.

Originally On Semiconductor had an internal spec for the zapping current during OTP programming with relatively wide limits. After extensive analysis of GDLC, it has been found out that the current during OTP programming is at the lower side of the spec window. A shift of the zapping current to the higher spec limit will improve the OTP programming performance at both On-Semi (in terms of Cpk improvement) and HELLA. Taking into account the importance of the OTP programming at HELLA, On-Semi proposes to narrow down the spec in order to improve the programming process.

This narrowing of the limits can be implemented by a small change of an internal resistor on GDLC. Functionality of the GDLC cannot be influenced as the change can only affect the OTP programming process. Therefore, all qualification results as obtained on the current version are also valid for the improved version.



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AFFECTED DEVICE LIST

0GDLC-003-XTP