

Product Bulletin

Document #:PB23124X Issue Date:13 Feb 2020

Title of Change:	FUSB307B datasheet change to add editorial fixes and register addition.		
Effective date:	13 Feb 2020		
Contact information:	Contact your local ON Semiconductor Sales Office or Jeremy.Ferris@onsemi.com		
Type of notification:	This Product Bulletin is for notification purposes only. ON Semiconductor will proceed with implementation of this change upon publication of this Product Bulletin.		
Change Category:	Datasheet		
Change Sub-Category(s):	Datasheet/Product Doc change		
Sites Affected:			
ON Semiconductor Sites		External Foundry/Subcon Sites	
None		None	

Description and Purpose:

Change	Explanation	
Changed register 28h's name from STD_IN_CAP to "Reserved".	The USB-IF TCPC spec specifies the register map and some registers or bits are optional. This is an optional one not implemented. All bits on this registers were already reserved.	
Changed STD_OUT_CAP (29h) register bits description to "Reserved".	These bits were not implemented and already read only with TCPC spec names. Changing to "Reserved" creates less confusion.	
Addition	Explanation	
Added Register B5h "RPVAL_OVERRIDE"	This register was hidden from customer as it's only used in niche applications. E.g. Factory testers. Use of this register is optional for majority of applications	

The change will not impact form, fit, or function of product.

List of Affected Standard Parts:

Note: Only the standard (off the shelf) part numbers are listed in the parts list. Any custom parts affected by this PCN are shown in the customer specific PCN addendum in the PCN email notification, or on the **PCN Customized Portal**.

FUSB307BMPX	

TEM001796 Rev. B Page 1 of 1