

# MECHANICAL CASE OUTLINE

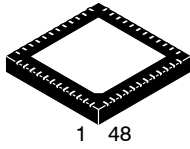
## PACKAGE DIMENSIONS

ON Semiconductor®



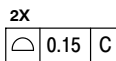
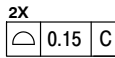
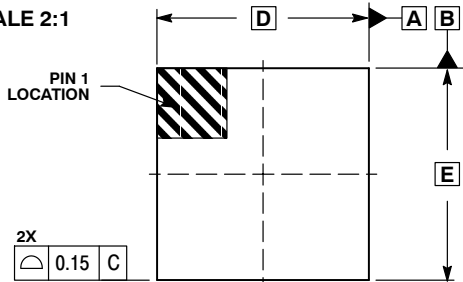
**QFN48 7x7, 0.5P**  
**CASE 485AJ-01**  
**ISSUE O**

DATE 27 APR 2007

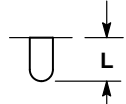


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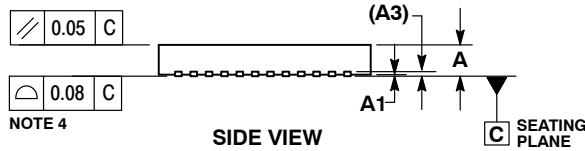
SCALE 2:1



TOP VIEW



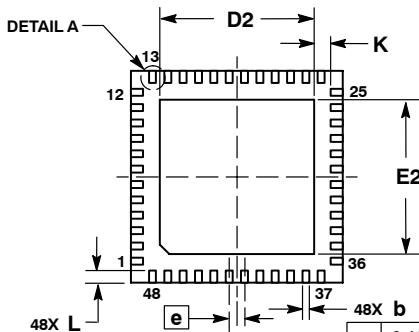
DETAIL A  
 OPTIONAL CONSTRUCTION  
 2X SCALE



NOTE 4

SIDE VIEW

SEATING PLANE



BOTTOM VIEW

|            |      |   |   |   |
|------------|------|---|---|---|
| $\text{M}$ | 0.10 | C | A | B |
| $\text{N}$ | 0.05 | C |   |   |

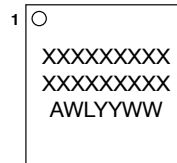
NOTE 3

NOTES:

1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO THE PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 MM FROM TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

| DIM | MILLIMETERS |      |
|-----|-------------|------|
|     | MIN         | MAX  |
| A   | 0.80        | 1.00 |
| A1  | 0.00        | 0.05 |
| A3  | 0.20 REF    |      |
| b   | 0.20        | 0.30 |
| D   | 7.00 BSC    |      |
| D2  | 5.00        | 5.20 |
| E   | 7.00 BSC    |      |
| E2  | 5.00        | 5.20 |
| e   | 0.50 BSC    |      |
| K   | 0.20        | ---  |
| L   | 0.30        | 0.50 |

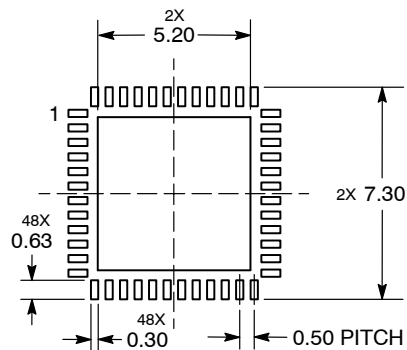
**GENERIC MARKING DIAGRAM\***



A = Assembly Location  
 WL = Wafer Lot  
 YY = Year  
 WW = Work Week

\*This information is generic. Please refer to device data sheet for actual part marking.  
 Pb-Free indicator, "G" or microdot "•", may or may not be present.

**SOLDERING FOOTPRINT\***



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

|                         |                         |  |
|-------------------------|-------------------------|--|
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| <b>DESCRIPTION:</b>     | <b>QFN48 7X7, 0.50P</b> | <b>PAGE 1 OF 1</b>   |

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