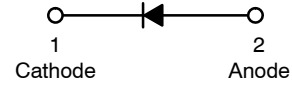


# 200 mA, 30 V Schottky Barrier Diode



## NSR02F30MX

These Schottky barrier diodes are optimized for low forward voltage drop and low leakage current that offers the most optimal power dissipation in applications. They are housed in a spacing saving x3DFN 0201 package ideal for space constraint applications.

### Features

- Low Forward Voltage Drop – 500 mV (Typ.) @  $I_F = 200$  mA
- Low Reverse Current – 20  $\mu$ A (Typ.) @  $V_R = 30$  V
- 200 mA of Continuous Forward Current
- ESD Rating – Human Body Model: Class 2  
 – Machine Model: Class M3  
 – CDM: Class IV
- High Switching Speed
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### Typical Applications

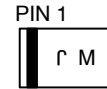
- LCD and Keypad Backlighting
- Camera Photo Flash
- Buck and Boost dc-dc Converters
- Reverse Voltage and Current Protection
- Clamping and Protection

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Reverse Voltage	$V_R$	30	V
Forward Current (DC)	$I_F$	200	mA
Forward Surge Current (60 Hz @ 1 cycle)	$I_{FSM}$	2	A
Repetitive Peak Forward Current (Pulse Wave = 1 sec, Duty Cycle = 66%)	$I_{FRM}$	1	A
ESD Rating: Human Body Model Machine Model	ESD	2 – 4 >400	kV V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

### MARKING DIAGRAM



J = Specific Device Code  
 (Rotated 180°)  
 M = Month Code

### ORDERING INFORMATION

Device	Package	Shipping†
NSR02F30MXT5G	X3DFN (Pb-Free)	10000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# NSR02F30MX

**Table 1. THERMAL CHARACTERISTICS**

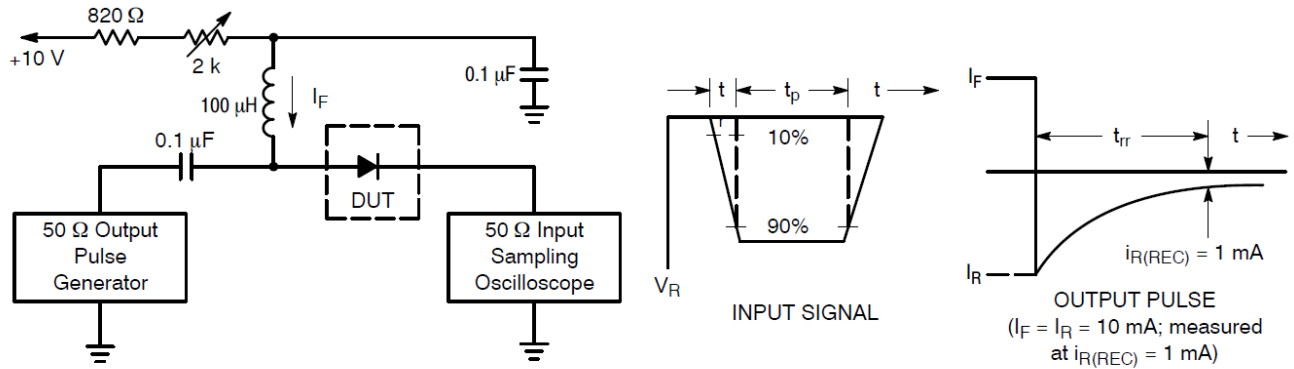
Rating	Symbol	Max	Unit
Thermal Resistance Junction-to-Ambient (Note 1) Total Power Dissipation @ TA = 25°C	$R_{\theta JA}$ $P_D$	695 180	°C/W mW
Storage Temperature Range	$T_{stg}$	-55 to +125	°C
Junction Temperature	$T_J$	+125	°C

1. Mounted onto a 4 in square FR-4 board 100 mm sq. 2 oz. Cu 0.06" thick single sided. Operating to steady state.

**Table 2. ELECTRICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

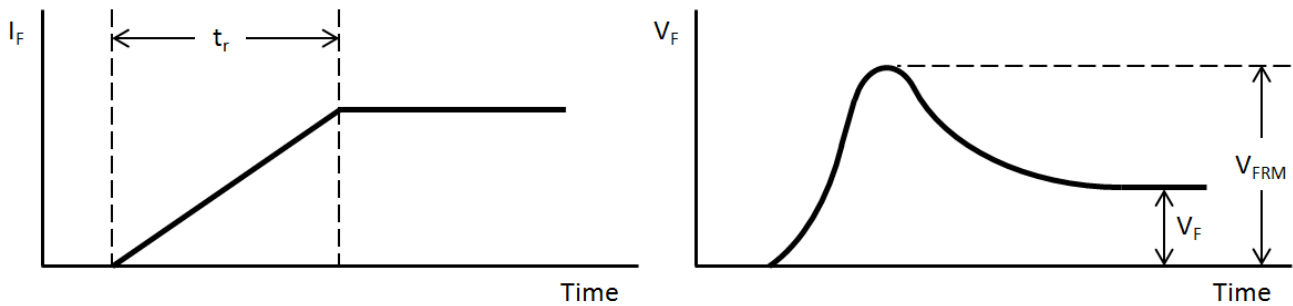
Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
Reverse Leakage	$V_R = 10\text{ V}$	$I_R$	-	-	15	$\mu\text{A}$
Reverse Leakage	$V_R = 30\text{ V}$	$I_R$	-	20	50	$\mu\text{A}$
Forward Voltage	$I_F = 1\text{ mA}$	$V_F$	-	155		mV
Forward Voltage	$I_F = 10\text{ mA}$	$V_F$	-	250	290	mV
Forward Voltage	$I_F = 100\text{ mA}$	$V_F$	-	375	490	mV
Forward Voltage	$I_F = 200\text{ mA}$	$V_F$	-	500	600	mV
Total Capacitance	$V_R = 1.0\text{ V}$ , $f = 1.0\text{ MHz}$	$C_T$	-	6	8	pF
Reverse Recovery Time	$I_F = I_R = 10\text{ mA}$ , $I_{R(REC)} = 1.0\text{ mA}$ , Figure 2	$t_{rr}$	-	2.4	3	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



- Notes: 1. A 2.0 kΩ variable resistor adjusted for a Forward Current ( $I_F$ ) of 10 mA.  
 2. Input pulse is adjusted so  $I_{R(\text{peak})}$  is equal to 10 mA.  
 3.  $t_p \gg t_{rr}$

**Figure 1. Recovery Time Equivalent Test Circuit**



**Figure 2. Peak Forward Recover Voltage Definition**

# NSR02F30MX

## TYPICAL CHARACTERISTICS

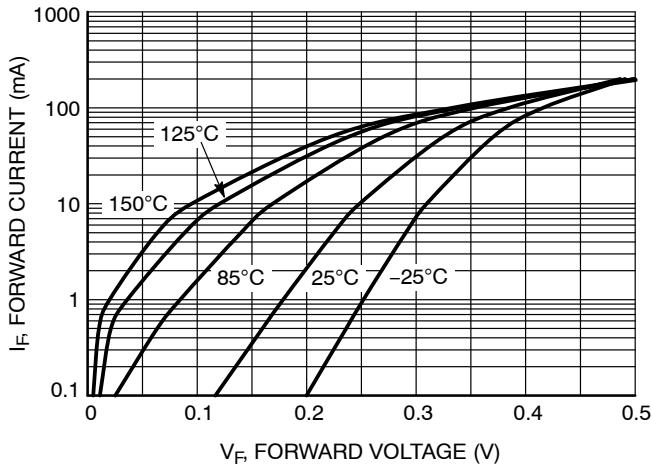


Figure 3. Forward Voltage

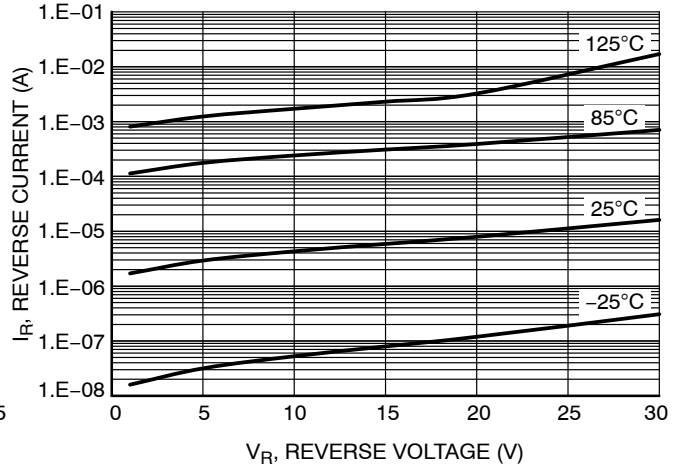


Figure 4. Leakage Current

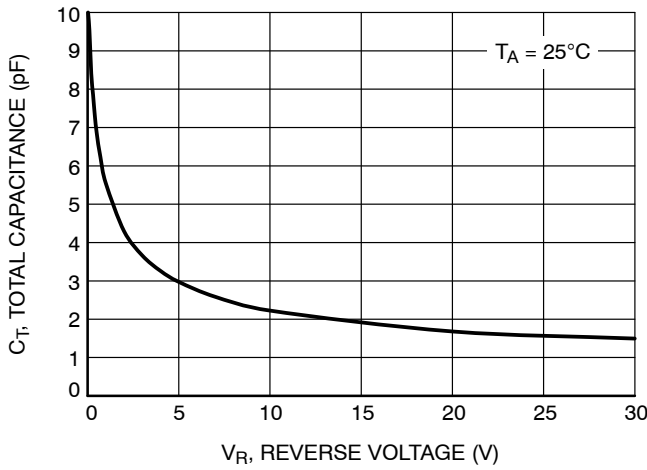


Figure 5. Total Capacitance

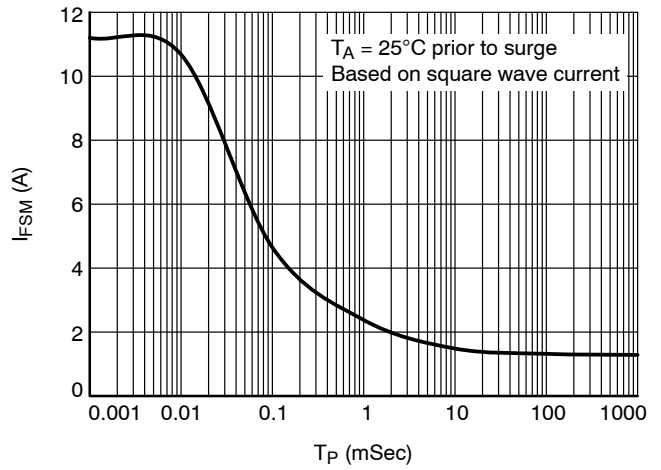


Figure 6. Forward Surge Current

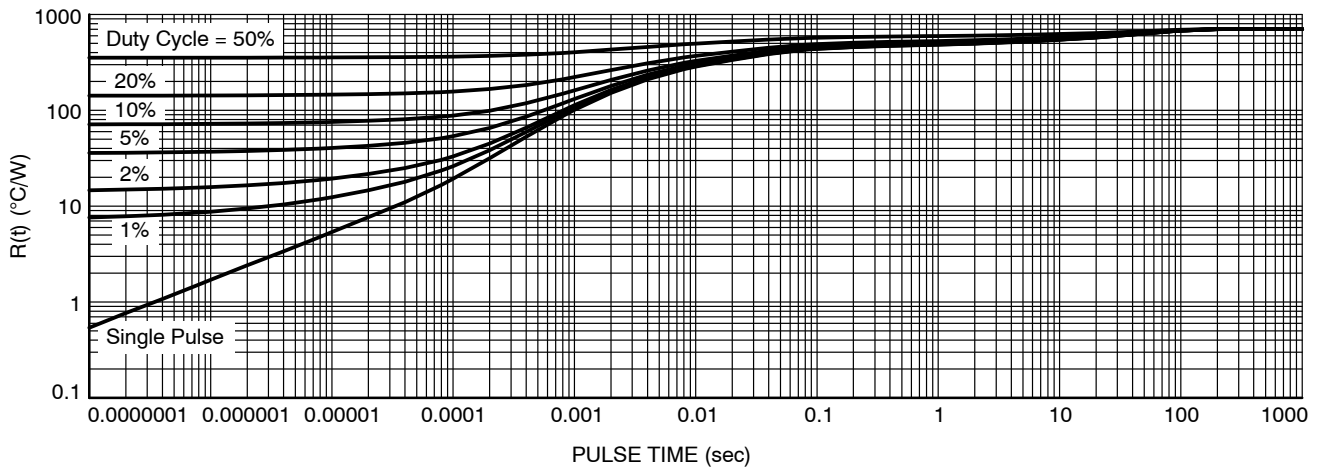
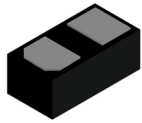


Figure 7. Thermal Response

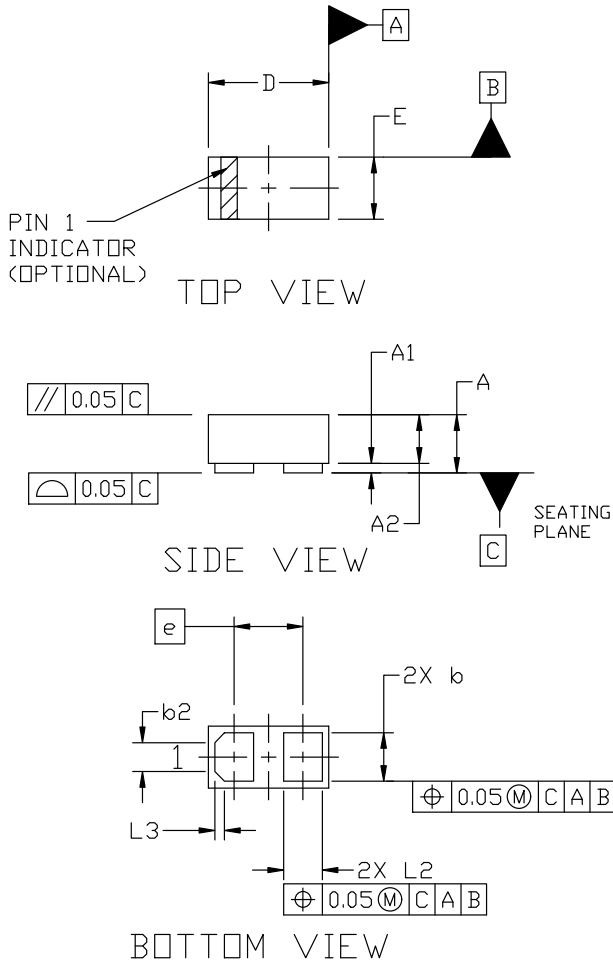
# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS



**X3DFN2 0.62x0.32x0.24, 0.35P**  
CASE 152AF  
ISSUE C

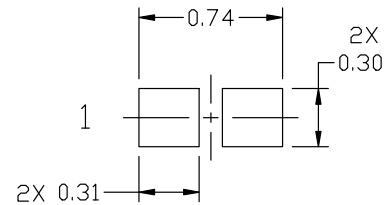
DATE 08 AUG 2023



**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. 0201

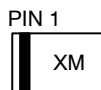
MILLIMETERS			
DIM	MIN.	NOM.	MAX.
A	0.25	0.29	0.33
A1	0.00	---	0.05
A2	0.14	0.24	0.34
b	0.22	0.25	0.28
b2	0.150 REF		
D	0.58	0.62	0.66
E	0.28	0.32	0.36
e	0.355 BSC		
L2	0.17	0.20	0.23
L3	0.050 REF		



**RECOMMENDED MOUNTING FOOTPRINT\***

\* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

**GENERIC MARKING DIAGRAM\***



X = Specific Device Code  
M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

<b>DOCUMENT NUMBER:</b>	<b>98AON56472E</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>X3DFN2 0.62x0.32x0.24, 0.35P</b>	<b>PAGE 1 OF 1</b>

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at [www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)

