



Quick Start Guide – NB1200x GUI NB3N1200K,NB3W1200L



Software Installation

- Unzip the distribution archive “NB3N1200x_GUI.zip”
 - all files are contained in a parent folder (NB3N1200x_GUI) which you can un-zip anywhere on your PC
- Look in the parent folder:
 - you will see a file: “NB3N1200x_GUI.exe”..
- Make a shortcut to that file and place it on your desktop, start menu, etc.
- That’s it!
 - there is no manipulation of the registry or PATH variables
 - to un-install, just delete the files



Software Use and Hardware Initialization

- Connect the eval board to a USB port on the PC.
- Allow Windows to install the necessary drivers for the eval board USB interface hardware...it will go out to the web to find them.
- Start the program using the shortcut that you made earlier.

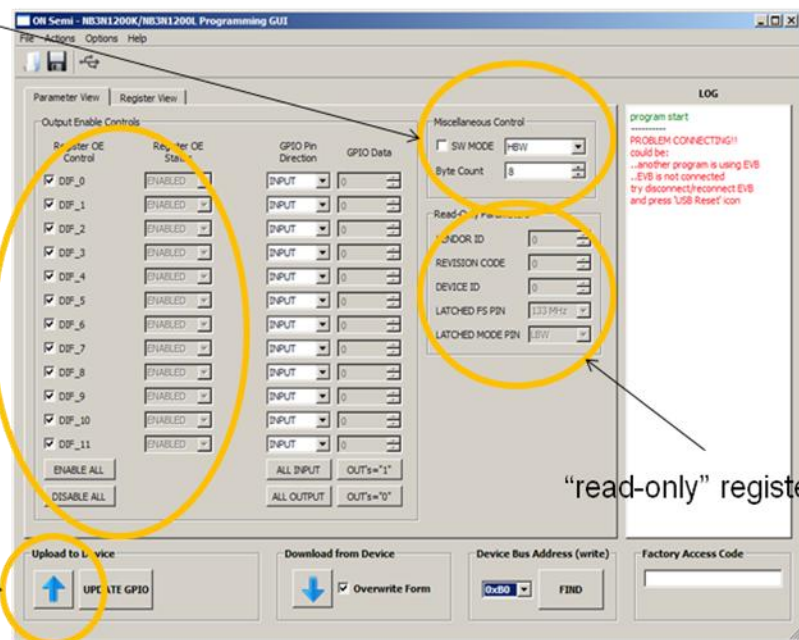


Overview – SMBus activities

other control registers: override BW, byte count...

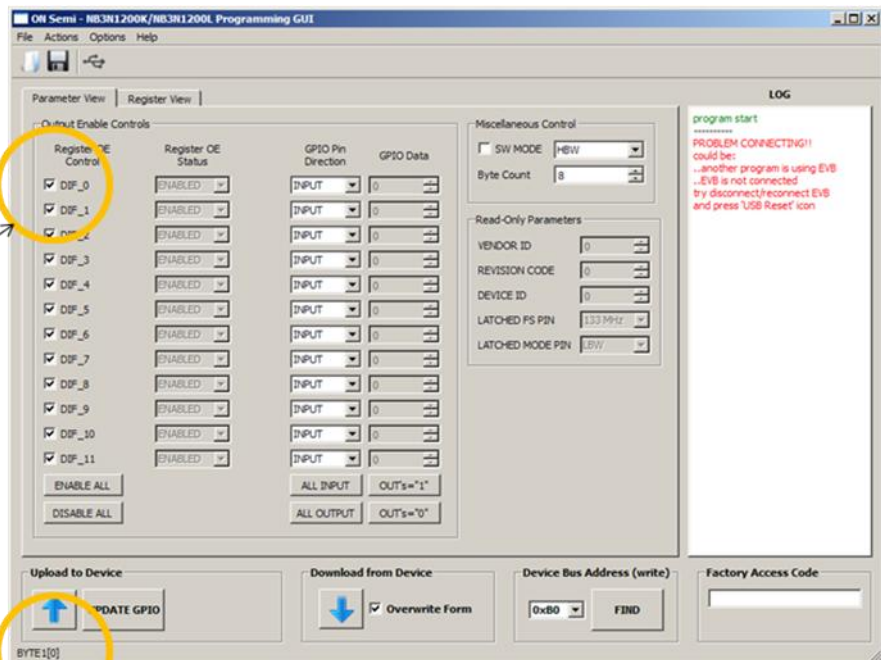
software output enable control & software readback of OE pin status

click the blue UP arrow to write and readback all SMBus registers when ready



“read-only” registers

hover over items with mouse to see which byte and bits are controlled in the “status bar”





Overview – SMBus activities cont.#1

read device registers
and (optionally)
update the values on
the form – sometimes
useful to determine
the state of a device

Parameter View | Register View |

Output Enable Controls

Register OE Control	Register OE Status	GPIO Pin Direction	GPIO Data
<input checked="" type="checkbox"/> DIF_0	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_1	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_2	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_3	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_4	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_5	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_6	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_7	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_8	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_9	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_10	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_11	ENABLED	INPUT	0

ENABLE ALL
DISABLE ALL

ALL INPUT
ALL OUTPUT

OUTs="1"
OUTs="0"

Miscellaneous Control

☐ SW MODE: HW

Byte Count: 8

Read-Only Parameters

VENDOR ID: 0

REVISION CODE: 0

DEVICE ID: 0

LATCHED FS PIN: 133 MHz

LATCHED MODE PIN: EVB

Upload to Device

UPDATE GPIO

Download from Device

Overwrite Form

Device Bus Address (write): 0x80

FIND

Factory Access Code

LOG

program start

PROBLEM CONNECTING!! could be:
...another program is using EVB
...EVB is not connected
try disconnect/reconnect EVB
and press USB Reset icon

shows SMBus
address where device
was found, or can re-
find the device if
address is changed

Parameter View | Register View |

Output Enable Controls

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<input checked="" type="checkbox"/> DIF_4	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_5	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_6	ENABLED	INPUT	0
<input checked="" type="checkbox"/> DIF_7	ENABLED	INPUT	0
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Upload to Device

UPDATE GPIO

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Overwrite Form

Device Bus Address (write): 0x80

FIND

Factory Access Code

LOG

program start

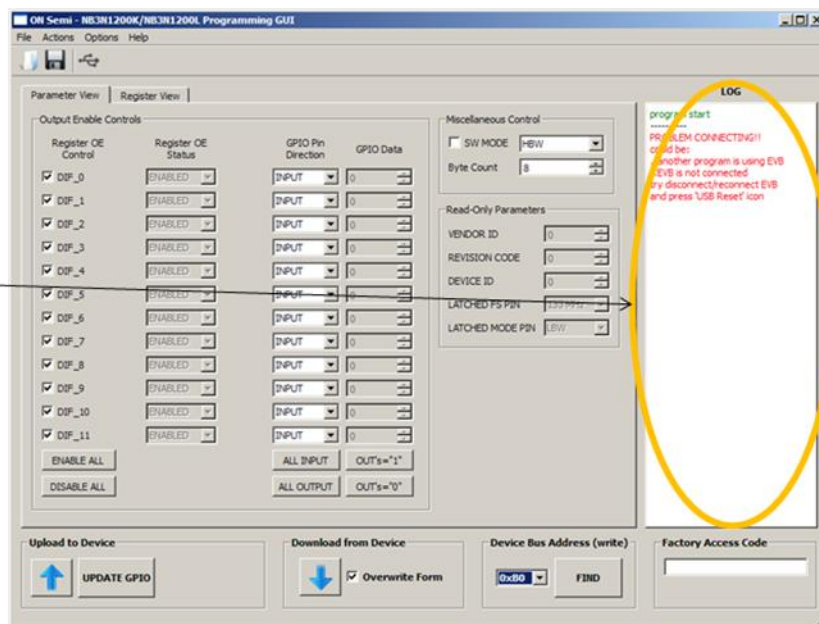
PROBLEM CONNECTING!! could be:
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try disconnect/reconnect EVB
and press USB Reset icon



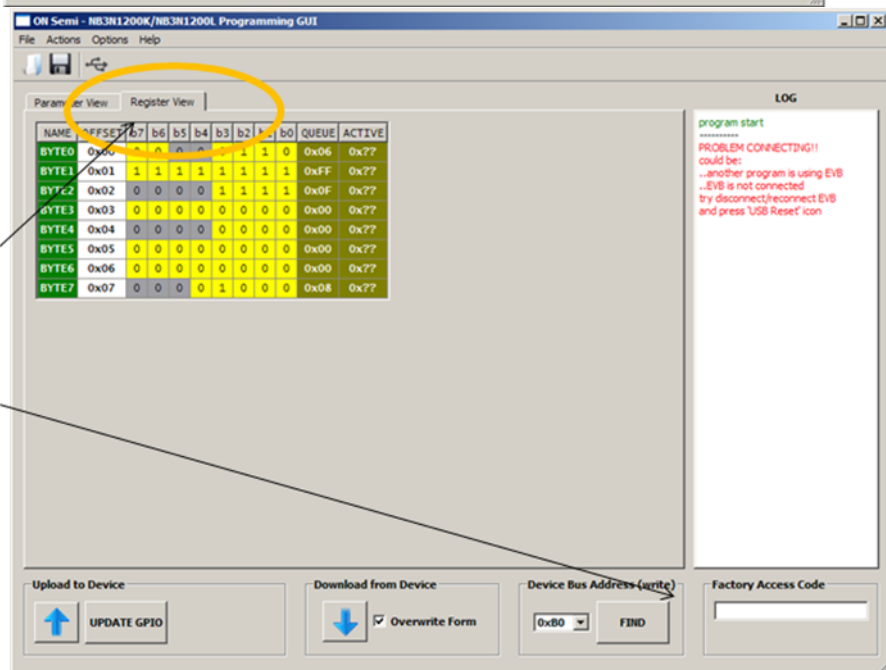
Overview – SMBus activities cont. #2

Overview - general

GUI interface log screen. This log will provide status of the interface link.



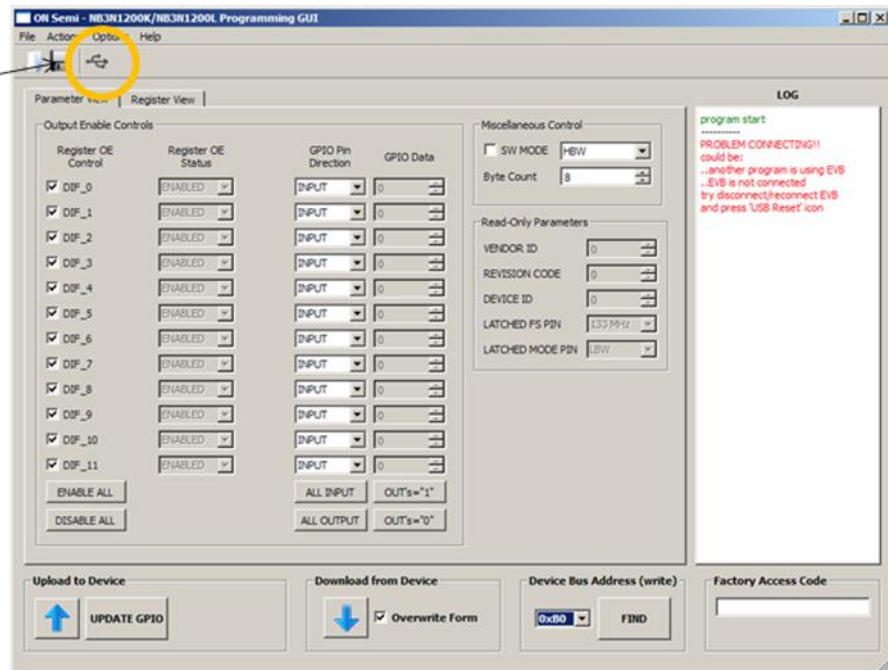
click on Register View tab to see currently "queued" values and what was last read back from the device. Byte 8,9 are locked out for vendor use.





Overview – SMBus activities cont. #3

reset USB connection
to eval board if
necessary



Power up sequence:

- 1.) Power off
- 2.) Connect PWRGD/PWRDN# pin to low via hardware jumper.
- 3.) Power on, VCC & VCCIO to 3.3Vdc.
- 4.) Turn on Differential clock signal (HCSL input, VIL=0V, VIH=700mV, Frequency 100 or 133 MHz.
- 5.) Move PWRGD/PWRDN# pin to high via hardware jumper.

Functionality that require power-up sequencing.

100M_133M# needs to be established with hardware select via jumper then power-up sequence followed. This is

HBW_BYPASS_LBW# needs to be established with hardware select via jumper then power-up sequence followed.