# **BF720T1G**, **SBF720T1G, BF720T3G**

# **NPN Silicon Transistor**

#### **Features**

- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant\*

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V <sub>CEO</sub>	300	Vdc
Collector - Base Voltage	V <sub>CBO</sub>	300	Vdc
Collector - Emitter Voltage	V <sub>CER</sub>	300	Vdc
Emitter - Base Voltage	V <sub>EBO</sub>	5.0	Vdc
Collector Current	I <sub>C</sub>	100	mAdc
Total Power Dissipation up to T <sub>A</sub> = 25°C	P <sub>D</sub>	1.5	W
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C
Junction Temperature	TJ	150	°C

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{ heta JA}$	83.3	°C/W

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Device mounted on a glass epoxy printed circuit board 1.575 in. x 1.575 in. x 0.059 in.; mounting pad for the collector lead min. 0.93 in<sup>2</sup>.



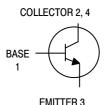
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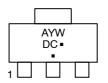
## NPN SILICON TRANSISTOR SURFACE MOUNT



SOT-223 (TO-261) **CASE 318E** STYLE 1



### MARKING DIAGRAM



= Assembly Location

= Year

W = Work Week

DC = Device Code

= Pb-Free Package

(Note: Microdot may be in either location)

### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
BF720T1G	SOT-223 (Pb-Free)	1,000 / Tape & Reel
SBF720T1G	SOT-223 (Pb-Free)	1,000 / Tape & Reel
BF720T3G	SOT-223 (Pb-Free)	4,000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# BF720T1G, SBF720T1G, BF720T3G

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristics	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	·			
Collector-Emitter Breakdown Voltage ( $I_C = 1.0 \text{ mAdc}, I_B = 0$ )	V <sub>(BR)</sub> CEO	300	_	Vdc
Collector-Base Breakdown Voltage ( $I_C = 100 \mu Adc, I_E = 0$ )	V <sub>(BR)CBO</sub>	300	-	Vdc
Collector-Emitter Breakdown Voltage ( $I_C$ = 100 $\mu$ Adc, $R_{BE}$ = 2.7 $k\Omega$ )	V <sub>(BR)</sub> CER	300	-	Vdc
Emitter-Base Breakdown Voltage ( $I_E = 10 \mu Adc, I_C = 0$ )	V <sub>(BR)EBO</sub>	5.0	-	Vdc
Collector-Base Cutoff Current (V <sub>CB</sub> = 200 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	-	10	nAdc
Collector–Emitter Cutoff Current ( $V_{CE}$ = 250 Vdc, $R_{BE}$ = 2.7 k $\Omega$ ) ( $V_{CE}$ = 200 Vdc, $R_{BE}$ = 2.7 k $\Omega$ , $T_{J}$ = 150°C)	I <sub>CER</sub>	- -	50 10	nAdc μAdc
ON CHARACTERISTICS			•	•
DC Current Gain (I <sub>C</sub> = 25 mAdc, V <sub>CE</sub> = 20 Vdc)	h <sub>FE</sub>	50	-	-
Collector-Emitter Saturation Voltage (I <sub>C</sub> = 30 mAdc, I <sub>B</sub> = 5.0 mAdc)	V <sub>CE(sat)</sub>	-	0.6	Vdc
DYNAMIC CHARACTERISTICS			•	•
Current-Gain - Bandwidth Product (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 10 Vdc, f = 35 MHz)	f <sub>T</sub>	60	-	MHz
Feedback Capacitance (V <sub>CE</sub> = 30 Vdc, I <sub>C</sub> = 0, f = 1.0 MHz)	C <sub>re</sub>	-	1.6	pF

## BF720T1G, SBF720T1G, BF720T3G

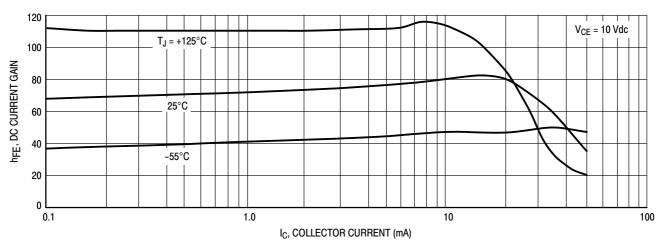


Figure 1. DC Current Gain

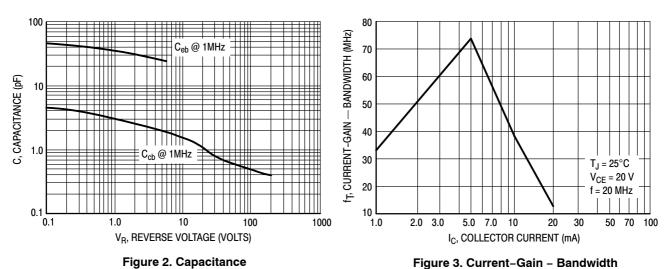
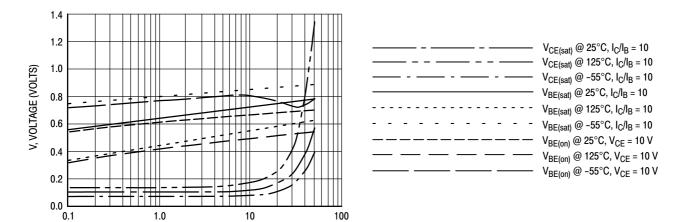


Figure 2. Capacitance



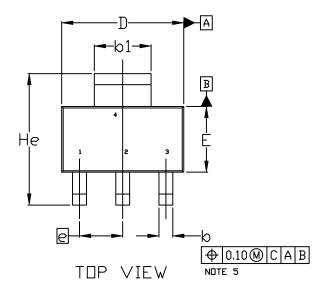
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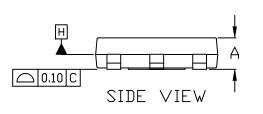
IC, COLLECTOR CURRENT (mA) Figure 4. "ON" Voltages

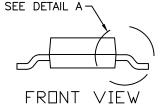


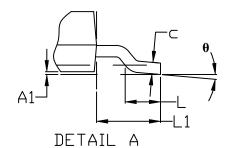
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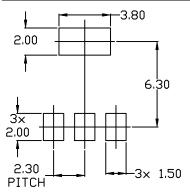




#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- DIMENSIONS D & E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS, MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.200MM PER SIDE.
- 4. DATUMS A AND B ARE DETERMINED AT DATUM H.
- 5. ALLIS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT OF THE PACKAGE BODY.
- 6. POSITIONAL TOLERANCE APPLIES TO DIMENSIONS 6 AND 61.

	MILLIMETERS			
DIM	MIN.	N□M.	MAX.	
Α	1.50	1.63	1.75	
A1	0.02	0.06	0.10	
b	0.60	0.75	0.89	
b1	2.90	3.06	3.20	
c	0.24	0.29	0.35	
D	6.30	6.50	6.70	
E	3.30	3.50	3.70	
е	2.30 BSC			
L	0.20			
L1	1.50	1.75	2.00	
He	6.70	7.00	7.30	
θ	0°		10°	



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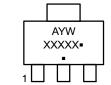
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STYLE 1: PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR	STYLE 2: PIN 1. ANODE 2. CATHODE 3. NC 4. CATHODE	STYLE 3: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN	STYLE 4: PIN 1. SOURCE 2. DRAIN 3. GATE 4. DRAIN	STYLE 5: PIN 1. DRAIN 2. GATE 3. SOURCE 4. GATE
STYLE 6: PIN 1. RETURN 2. INPUT 3. OUTPUT 4. INPUT	STYLE 7: PIN 1. ANODE 1 2. CATHODE 3. ANODE 2 4. CATHODE	STYLE 8: CANCELLED	STYLE 9: PIN 1. INPUT 2. GROUND 3. LOGIC 4. GROUND	STYLE 10: PIN 1. CATHODE 2. ANODE 3. GATE 4. ANODE
STYLE 11: PIN 1. MT 1 2. MT 2 3. GATE 4. MT 2	STYLE 12: PIN 1. INPUT 2. OUTPUT 3. NC 4. OUTPUT	STYLE 13: PIN 1. GATE 2. COLLECTOR 3. EMITTER 4. COLLECTOR		

# GENERIC MARKING DIAGRAM\*



A = Assembly Location

Y = Year W = Work Week

 $XXXXX \ = Specific \ Device \ Code$ 

= Pb-Free Package

(Note: Microdot may be in either location)
\*This information is generic. Please refer to
device data sheet for actual part marking.
Pb-Free indicator, "G" or microdot "•", may
or may not be present. Some products may
not follow the Generic Marking.

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