

# BC857BTT1G

## General Purpose Transistor

### PNP Silicon

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-416/SC-75 which is designed for low power surface mount applications.

#### Features

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- This is a Pb-Free Device

#### MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )

Rating	Symbol	Max	Unit
Collector-Emitter Voltage	$V_{CEO}$	-45	V
Collector-Base Voltage	$V_{CBO}$	-50	V
Emitter-Base Voltage	$V_{EBO}$	-5.0	V
Collector Current - Continuous	$I_C$	-100	mAdc
Collector Current - Peak	$I_C$	-200	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) $T_A = 25^\circ\text{C}$ Derated above $25^\circ\text{C}$	$P_D$	200	mW
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	600	$^\circ\text{C/W}$
Total Device Dissipation, FR-4 Board (Note 2) $T_A = 25^\circ\text{C}$ Derated above $25^\circ\text{C}$	$P_D$	300	mW
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	400	$^\circ\text{C/W}$
Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

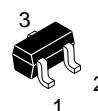
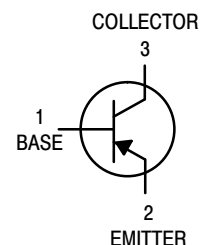
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-4 @ min pad.
2. FR-4 @  $1.0 \times 1.0$  in pad.



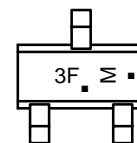
ON Semiconductor®

<http://onsemi.com>



CASE 463  
SOT-416  
STYLE 1

#### MARKING DIAGRAM



3F = Device Code  
M = Date Code  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation may vary depending upon manufacturing location.

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

# BC857BTT1G

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Collector–Emitter Breakdown Voltage ( $I_C = -10\text{ mA}$ )	$V_{(BR)CEO}$	-45	-	-	V
Collector–Emitter Breakdown Voltage ( $I_C = -10\text{ }\mu\text{A}$ , $V_{EB} = 0$ )	$V_{(BR)CES}$	-50	-	-	V
Collector–Base Breakdown Voltage ( $I_C = -10\text{ }\mu\text{A}$ )	$V_{(BR)CBO}$	-50	-	-	V
Emitter–Base Breakdown Voltage ( $I_E = -1.0\text{ }\mu\text{A}$ )	$V_{(BR)EBO}$	-5.0	-	-	V
Collector Cutoff Current ( $V_{CB} = -30\text{ V}$ ) ( $V_{CB} = -30\text{ V}$ , $T_A = 150^\circ\text{C}$ )	$I_{CBO}$	-	-	-15 -4.0	nA $\mu\text{A}$
<b>ON CHARACTERISTICS</b>					
DC Current Gain ( $I_C = -10\text{ }\mu\text{A}$ , $V_{CE} = -5.0\text{ V}$ ) ( $I_C = -2.0\text{ mA}$ , $V_{CE} = -5.0\text{ V}$ )	$h_{FE}$	- 220	150 290	- 475	-
Collector–Emitter Saturation Voltage ( $I_C = -10\text{ mA}$ , $I_B = -0.5\text{ mA}$ ) ( $I_C = -100\text{ mA}$ , $I_B = -5.0\text{ mA}$ )	$V_{CE(sat)}$	- -	- -	-0.3 -0.65	V
Base–Emitter Saturation Voltage ( $I_C = -10\text{ mA}$ , $I_B = -0.5\text{ mA}$ ) ( $I_C = -100\text{ mA}$ , $I_B = -5.0\text{ mA}$ )	$V_{BE(sat)}$	- -	-0.7 -0.9	- -	V
Base–Emitter On Voltage ( $I_C = -2.0\text{ mA}$ , $V_{CE} = -5.0\text{ V}$ ) ( $I_C = -10\text{ mA}$ , $V_{CE} = -5.0\text{ V}$ )	$V_{BE(on)}$	-0.6 -	- -	-0.75 -0.82	V
<b>SMALL–SIGNAL CHARACTERISTICS</b>					
Current–Gain – Bandwidth Product ( $I_C = -10\text{ mA}$ , $V_{CE} = -5.0\text{ Vdc}$ , $f = 100\text{ MHz}$ )	$f_T$	100	-	-	MHz
Output Capacitance ( $V_{CB} = -10\text{ V}$ , $f = 1.0\text{ MHz}$ )	$C_{ob}$	-	-	4.5	pF
Noise Figure ( $I_C = -0.2\text{ mA}$ , $V_{CE} = -5.0\text{ Vdc}$ , $R_S = 2.0\text{ k}\Omega$ , $f = 1.0\text{ kHz}$ , $BW = 200\text{ Hz}$ )	NF	-	-	10	dB

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

# BC857BTT1G

## TYPICAL CHARACTERISTICS

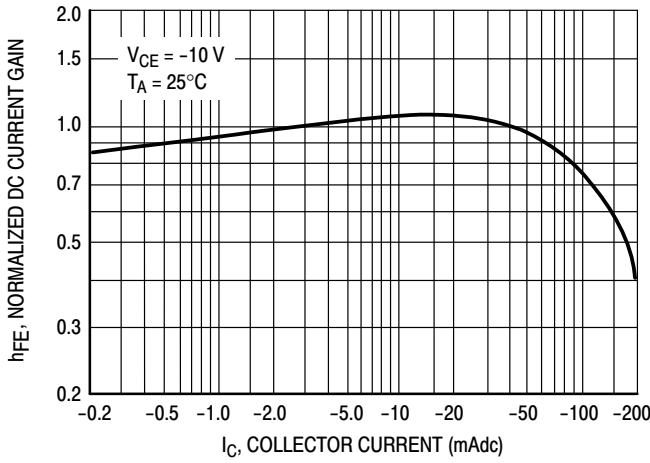


Figure 1. Normalized DC Current Gain

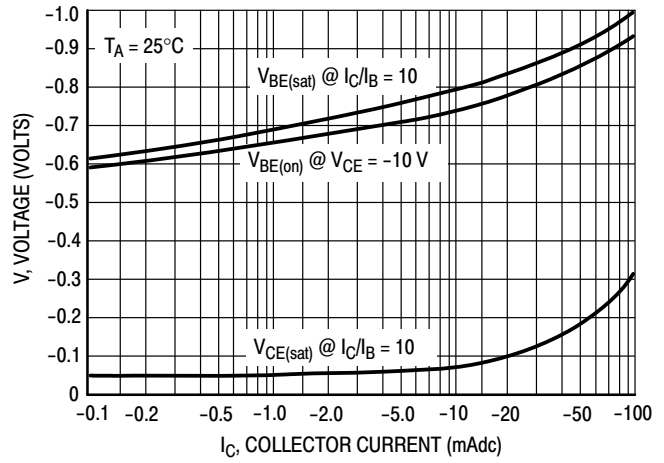


Figure 2. "Saturation" and "On" Voltages

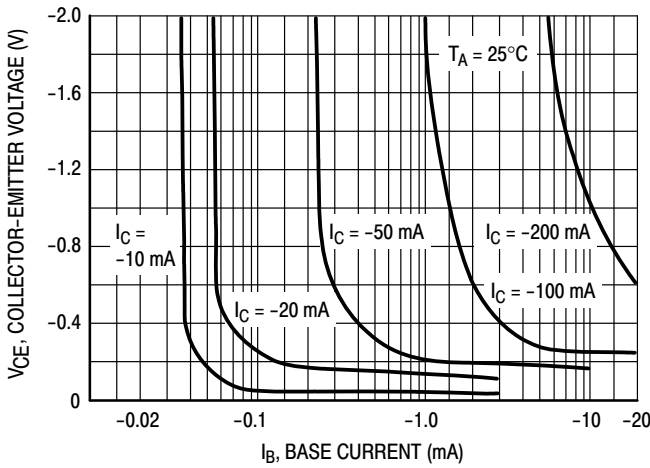


Figure 3. Collector Saturation Region

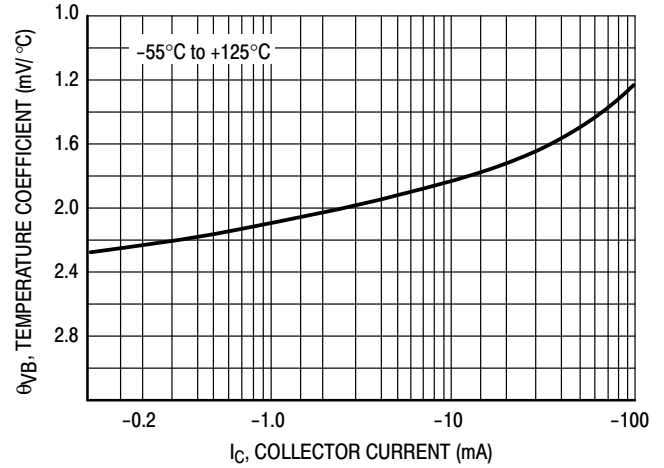


Figure 4. Base-Emitter Temperature Coefficient

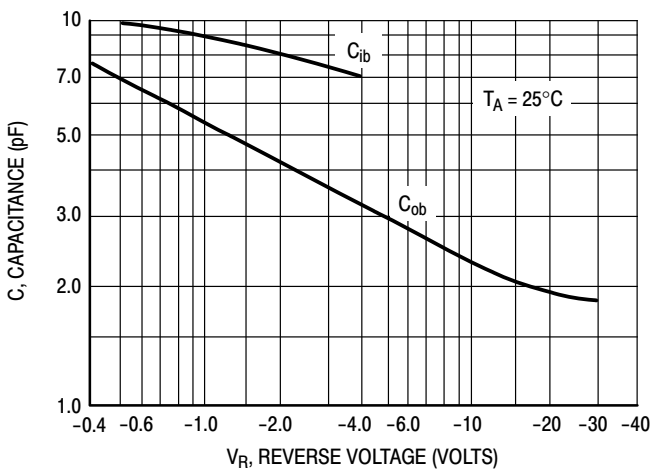


Figure 5. Capacitances

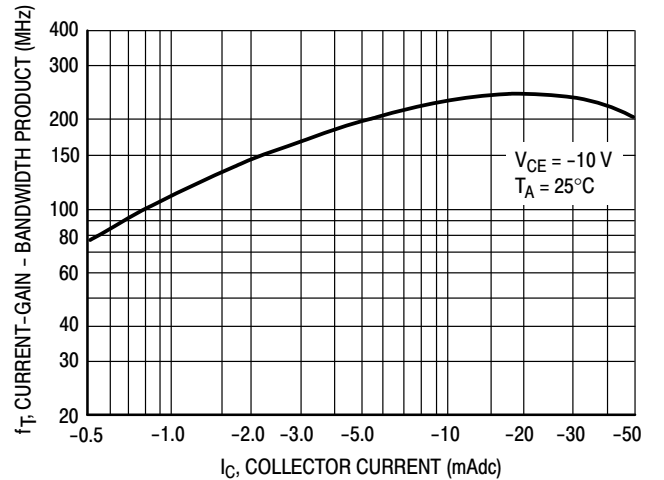


Figure 6. Current-Gain - Bandwidth Product

# BC857BTT1G

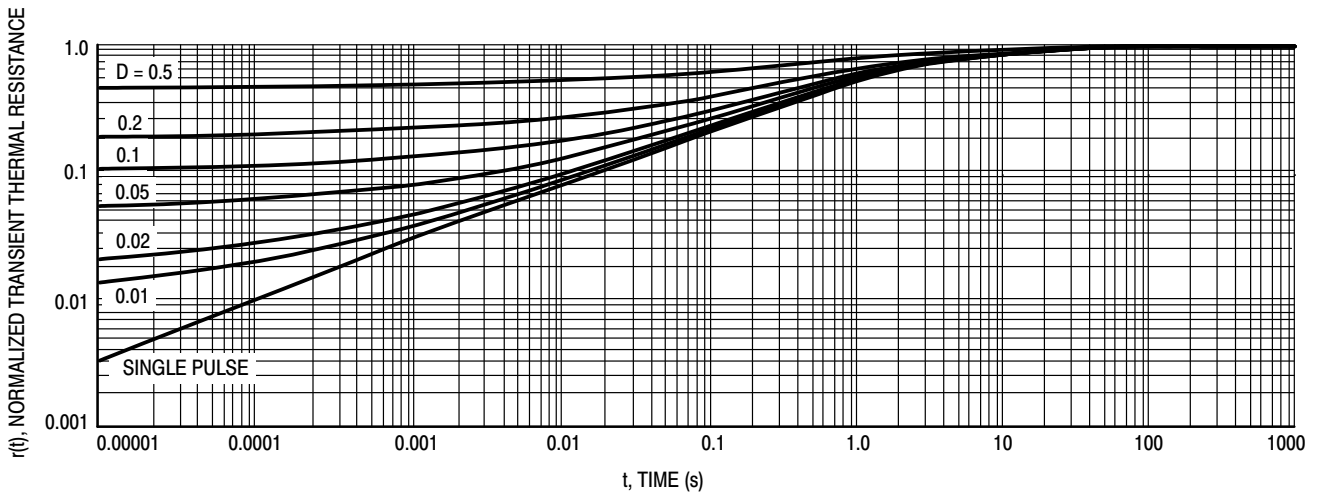


Figure 7. Thermal Response

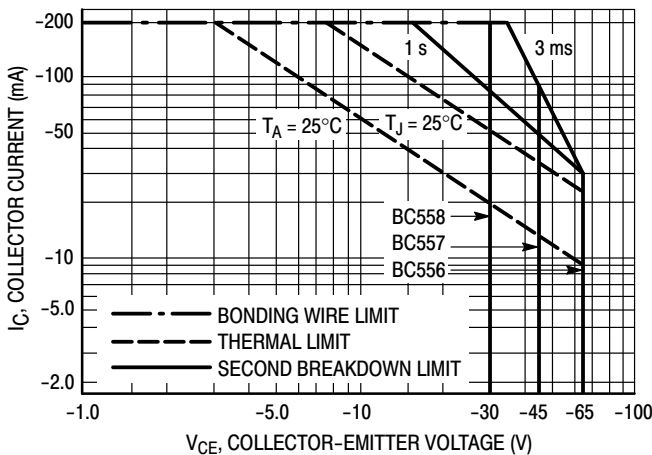


Figure 8. Active Region Safe Operating Area

The safe operating area curves indicate  $I_C$ - $V_{CE}$  limits of the transistor that must be observed for reliable operation. Collector load lines for specific circuits must fall below the limits indicated by the applicable curve.

The data of Figure 8 is based upon  $T_{J(pk)} = 150^\circ\text{C}$ ;  $T_C$  or  $T_A$  is variable depending upon conditions. Pulse curves are valid for duty cycles to 10% provided  $T_{J(pk)} \leq 150^\circ\text{C}$ .  $T_{J(pk)}$  may be calculated from the data in Figure 7. At high case or ambient temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by the secondary breakdown.

## ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
BC857BTT1G	SOT-416 (PB-Free)	3,000 / Tape & Reel
NSVBC857BTT1G*		

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

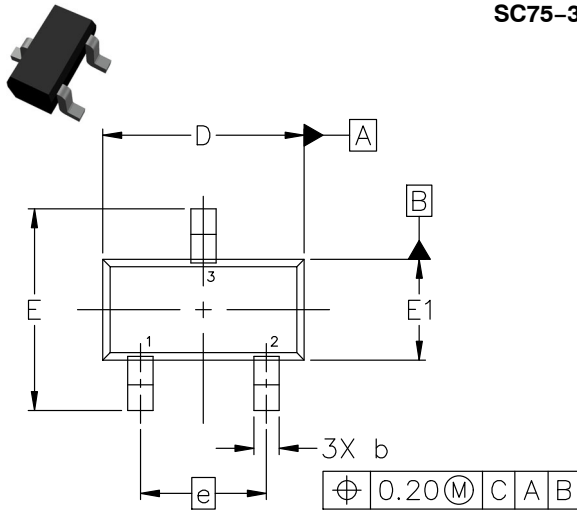
# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

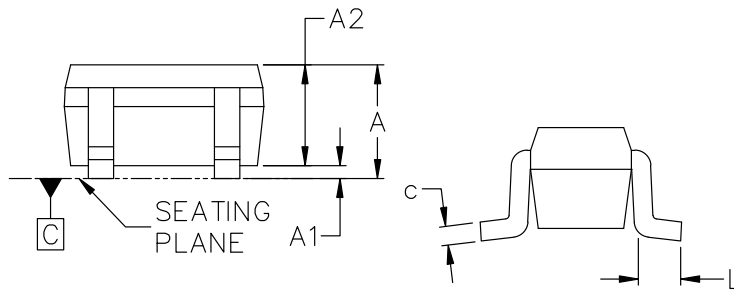


**SC75-3 1.60x0.80x0.80, 1.00P**  
**CASE 463**  
**ISSUE H**

DATE 01 FEB 2024



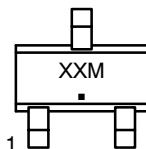
TOP VIEW



SIDE VIEW

END VIEW

### GENERIC MARKING DIAGRAM\*



- XX = Specific Device Code
- M = Date Code
- = Pb-Free Package

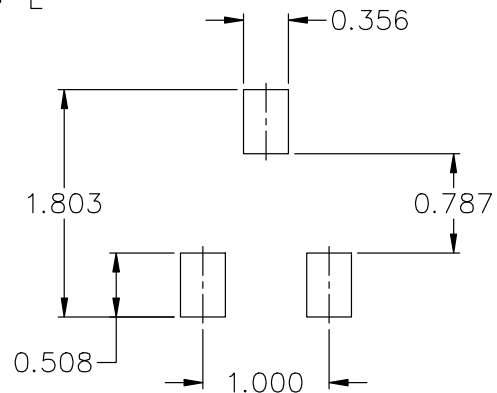
\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

- |   |  |  |
|---|--|--|
| STYLE 1:<br>PIN 1. BASE<br>2. EMITTER<br>3. COLLECTOR | STYLE 2:<br>PIN 1. ANODE<br>2. N/C<br>3. CATHODE | STYLE 3:<br>PIN 1. ANODE<br>2. ANODE<br>3. CATHODE |
| STYLE 4:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. ANODE  | STYLE 5:<br>PIN 1. GATE<br>2. SOURCE<br>3. DRAIN |  |

### NOTES:

- DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
- ALL DIMENSION ARE IN MILLIMETERS.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.70	0.80	0.90
A1	0.00	0.05	0.10
A2	0.80 REF.		
b	0.15	0.20	0.30
c	0.10	0.15	0.25
D	1.55	1.60	1.65
E	1.50	1.60	1.70
E1	0.70	0.80	0.90
e	1.00 BSC		
L	0.10	0.15	0.20



### RECOMMENDED MOUNTING FOOTPRINT\*

\* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

<b>DOCUMENT NUMBER:</b>	<b>98ASB15184C</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>SC75-3 1.60x0.80x0.80, 1.00P</b>	<b>PAGE 1 OF 1</b>

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at [www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)