

## RECOMMENDED MOUNTING FOOTPRINT\*

For additional information on our Pb-Free strategy and soldering details, please download the IIN Seniconductor Soldering and Mounting Techniques Reference Manual, SCILDERMYD.

## GENERIC MARKING DIAGRAM\*

XXXXXX XXXXXX AWLYWW XXXX = Specific Device Code A = Assembly Location

WL = Wafer Lot
Y = Year

WW = Work Week

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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