// 0.10 C

0.08 C

WLCSP8 2.92x1.53x0.525 CASE 567YR **ISSUE O**

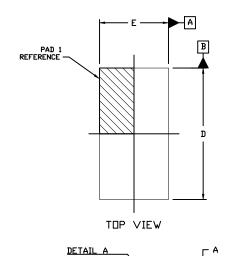
BACKSIDE COATING

DETAIL A

SEATING

PLANE

DATE 21 NOV 2019

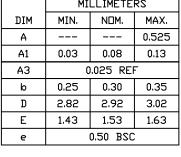


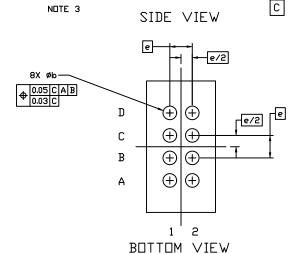
NDTES:

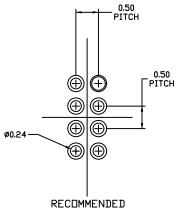
A3

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
- CONTROLLING DIMENSION: MILLIMETERS 2.
- COPLANARITY APPLIES TO THE SPHERICAL CROWNS OF THE SOLDER BALLS.

	MILLIMETERS		
DIM	MIN.	N□M.	MAX.
Α			0.525
A1	0.03	0.08	0.13
A3	0.025 REF		
b	0.25	0.30	0.35
D	2.82	2.92	3.02
E	1.43	1.53	1.63
е	0.50 BSC		







(NSMD PAD TYPE) For additional information on our Pb-Free strategy and soldering details, please download the IBN Seniconductor Soldering and Mounting Techniques Reference Manual, SILDERRM/D.

MOUNTING FOOTPRINT*

GENERIC MARKING DIAGRAM*

XXXXX XXXXX **ALYW**

XXXX = Specific Device Code

= Assembly Location

= Wafer Lot L = Year

= Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	WLCSP8 2.92x1.53x0.525		PAGE 1 OF 1	

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