

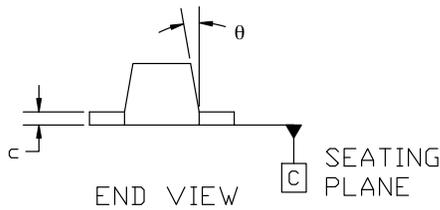
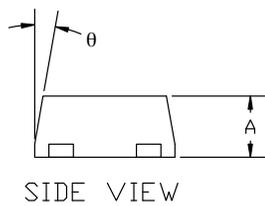
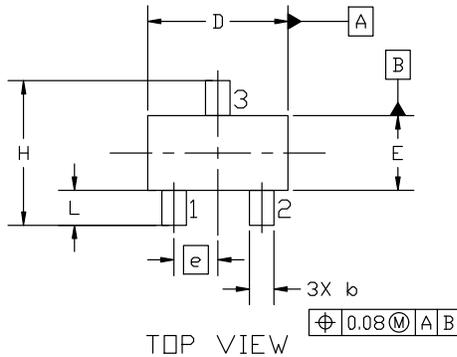
# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

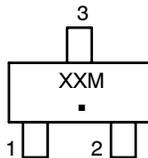


**SC-89 3LEAD 1.60x0.85x0.70, 0.50P**  
**CASE 463C**  
**ISSUE D**

DATE 20 FEB 2024



### GENERIC MARKING DIAGRAM\*



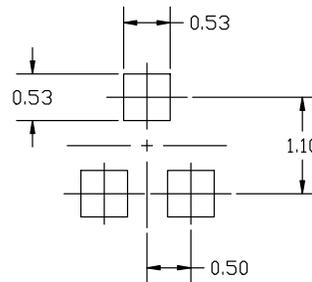
XX = Specific Device Code  
M = Date Code  
▪ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSIONS: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.60	0.70	0.80
b	0.23	0.28	0.33
c	0.10	0.15	0.20
D	1.50	1.60	1.70
E	0.75	0.85	0.95
e	0.50 BSC		
H	1.50	1.60	1.70
L	0.30	0.40	0.50
θ	---	---	10°



### RECOMMENDED MOUNTING FOOTPRINT

\* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

#### STYLE 1:

- PIN 1. BASE
- EMITTER
- COLLECTOR

#### STYLE 2:

- PIN 1. ANODE
- N/C
- CATHODE

#### STYLE 3:

- PIN 1. ANODE
- ANODE
- CATHODE

#### STYLE 4:

- PIN 1. CATHODE
- CATHODE
- ANODE

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<b>DESCRIPTION:</b>	<b>SC-89 3LEAD 1.60x0.85x0.70, 0.50P</b>	<b>PAGE 1 OF 1</b>

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